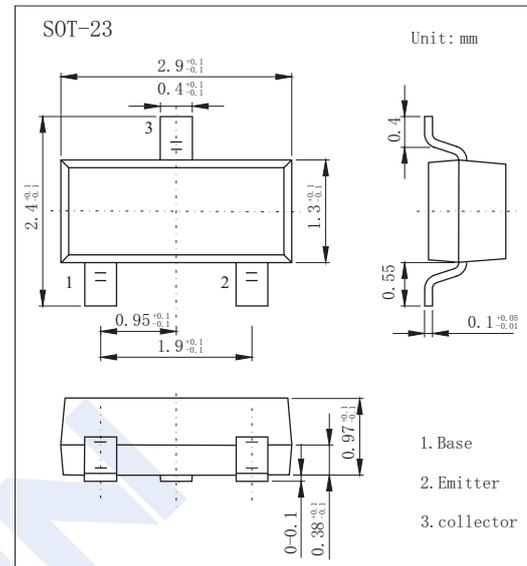


## PNP Transistor

## STR2550

## ■ Features

- Excellent  $h_{FE}$  linearity up to 50 mA
- The NPN complementary type is STR1550

■ Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}.$ )

Parameter	Symbol	Rating	Unit
Collector - Base Voltage	$V_{CBO}$	-500	V
Collector - Emitter Voltage	$V_{CEO}$	-500	
Emitter - Base Voltage	$V_{EBO}$	-7	
Collector Current (DC)	$I_C$	-0.5	A
Collector peak current ( $t_p < 5$ ms)	$I_{CM}$	-1	
Power Dissipation	$P_C$	500	mW
Thermal Resistance Junction to Ambient <sup>*1</sup>	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature range	$T_{stg}$	-55 to +150	

\*1. Device mounted on PCB area of 1 cm<sup>2</sup>.

## PNP Transistor

## STR2550

■ Electrical Characteristics (T<sub>c</sub> = 25 °C unless otherwise specified.)

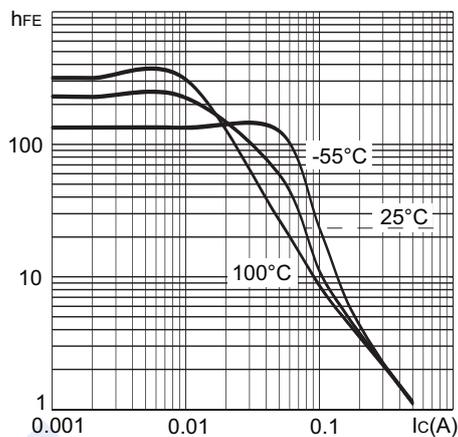
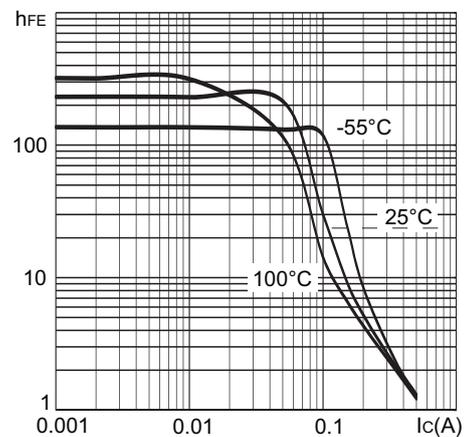
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector- base breakdown voltage	V <sub>CB0</sub>	I <sub>c</sub> = -100 μA , I <sub>E</sub> =0	-500			V
Collector- emitter breakdown voltage <sup>*1</sup>	V <sub>CEO</sub>	I <sub>c</sub> = -1 mA , I <sub>B</sub> =0	-500			
Emitter - base breakdown voltage	V <sub>EB0</sub>	I <sub>E</sub> = -100 μA , I <sub>C</sub> =0	-7			
Collector-base cut-off current	I <sub>CB0</sub>	V <sub>CB</sub> = -500V , I <sub>E</sub> =0			-10	μA
Emitter cut-off current	I <sub>EB0</sub>	V <sub>EB</sub> = -5V , I <sub>C</sub> =0			-1	
Collector-emitter saturation voltage <sup>*1</sup>	V <sub>CE(sat)</sub>	I <sub>C</sub> =-20mA, I <sub>B</sub> =-2mA			-0.2	V
		I <sub>C</sub> =-50mA, I <sub>B</sub> =-10mA			-0.3	
Base - emitter saturation voltage <sup>*1</sup>	V <sub>BE(sat)</sub>	I <sub>C</sub> = -50mA, I <sub>B</sub> =- 10mA			-1.0	
Base-emitter on voltage	V <sub>BE(on)</sub>	I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -10 V			-1.1	
DC current gain <sup>*1</sup>	h <sub>FE</sub>	V <sub>CE</sub> = -10 V, I <sub>C</sub> = - 1 mA	100			
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = - 50 mA	100		300	
		V <sub>CE</sub> = -10 V, I <sub>C</sub> = - 100 mA	10			

\*1.Pulse test: pulse duration ≤ 300 μs, duty cycle ≤ 2%

## ■ Marking

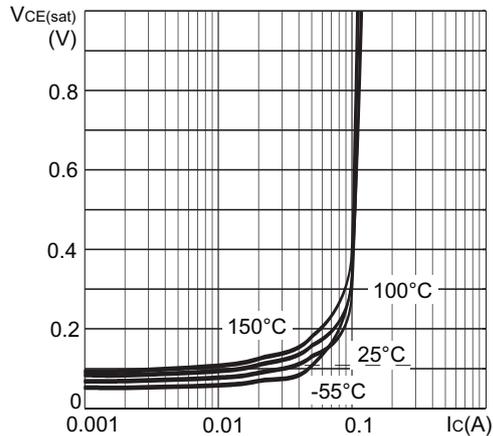
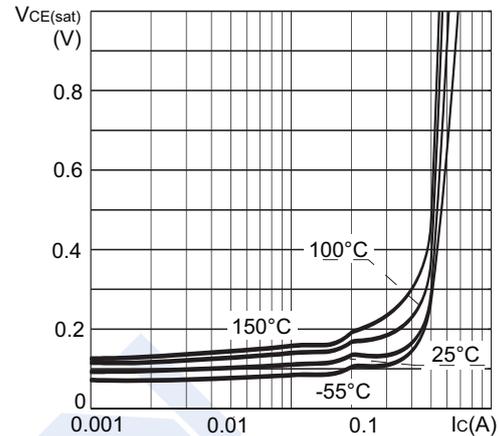
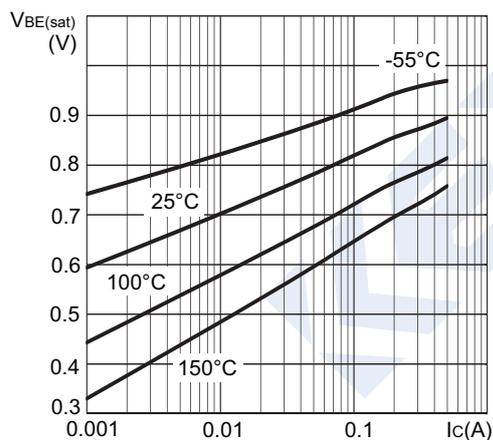
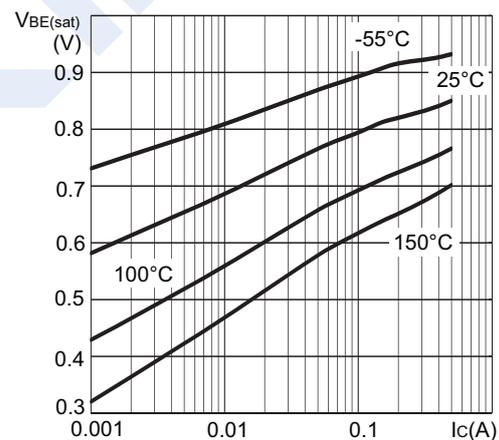
Marking	2550
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## ■ Typical Characteristics

Figure 1. h<sub>FE</sub> vs. I<sub>C</sub> @ V<sub>CE</sub>= 5 VFigure 2. h<sub>FE</sub> vs. I<sub>C</sub> @ V<sub>CE</sub>= 10 V

## PNP Transistor

## STR2550

Figure 3.  $V_{CE(sat)}$  vs.  $I_C$  @  $h_{FE} = 5$ Figure 4.  $V_{CE(sat)}$  vs.  $I_C$  @  $h_{FE} = 10$ Figure 5.  $V_{BE(sat)}$  vs.  $I_C$  @  $h_{FE} = 5$ Figure 6.  $V_{BE(sat)}$  vs.  $I_C$  @  $h_{FE} = 10$ Figure 7.  $V_{BE(on)}$  vs.  $I_C$  @  $V_{CE} = 10$  V