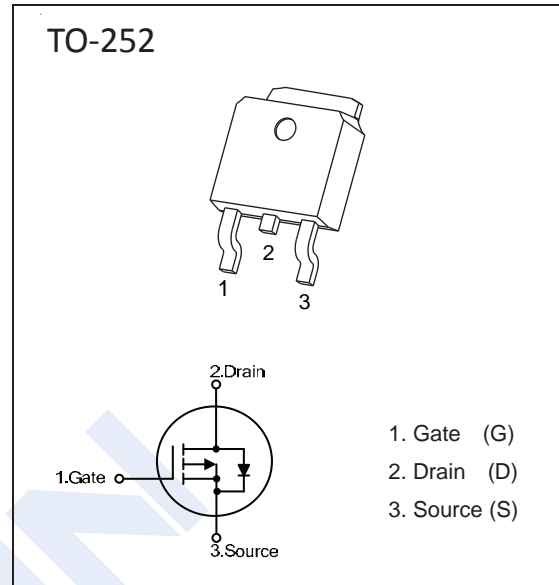


## P-Channel MOSFET

## NDT50P06

## ■ Features

- $V_{DS}$  (V) = -60V
- $I_D$  = -50 A
- $R_{DS(ON)}$  = 25m $\Omega$  (typ.) @  $V_{GS}$  = -10V

■ Absolute Maximum Ratings ( $T_a = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>*1</sup>	$I_D$	-50	A
Pulsed Drain Current <sup>*2</sup>	$I_{DM}$	-200	
Single Pulse Avalanche Energy <sup>*3</sup>	EAS	196	mJ
Power Dissipation <sup>*1</sup>	$P_D$	75	W
Thermal Resistance, Junction- to-Ambient <sup>*4</sup>	$R_{\theta JA}$	100	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction- to-Case <sup>*1</sup>	$R_{\theta JC}$	1.66	
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Junction Storage Temperature Range	$T_{stg}$	-55 to 150	

Notes: 1.  $T_c=25^\circ\text{C}$  Limited only by maximum temperature allowed.

2.  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ .

3. EAS condition:  $V_{DD}=-15\text{V}$ ,  $V_{GS}=-10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_g=25\Omega$  Starting  $T_J = 25^\circ\text{C}$ .

4. The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_a=25^\circ\text{C}$ .

## P-Channel MOSFET

## NDT50P06

■ Electrical Characteristics ( $T_a = 25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Off characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D = -250\mu\text{A}$ , $V_{GS} = 0\text{V}$	-60			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -48\text{V}$ , $V_{GS} = 0\text{V}$ , $T_J = 25^\circ\text{C}$			-1	$\mu\text{A}$
		$V_{DS} = -48\text{V}$ , $V_{GS} = 0\text{V}$ , $T_J = 125^\circ\text{C}$			-100	
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$			$\pm 100$	nA
<b>On characteristics</b> <sup>*5</sup>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = -250\mu\text{A}$	-1		-3	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = -10\text{V}$ , $I_D = -20\text{A}$		25	30	m $\Omega$
<b>Dynamic characteristics</b> <sup>*5,6</sup>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{V}$ , $V_{DS} = -25\text{V}$ , $f = 1\text{MHz}$		4500	7500	pF
Output Capacitance	$C_{oss}$			705	950	
Reverse Transfer Capacitance	$C_{rss}$			515	760	
Gate resistance	$R_g$	$f = 1\text{MHz}$		5.7		$\Omega$
<b>Switching characteristics</b> <sup>*5,6</sup>						
Total Gate Charge	$Q_g$	$V_{GS} = -10\text{V}$ , $V_{DS} = -30\text{V}$ , $I_D = -20\text{A}$		72	130	nC
Gate Source Charge	$Q_{gs}$			15	29	
Gate Drain Charge	$Q_{gd}$			17	32	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = -30\text{V}$ , $R_G = 3\Omega$ , $R_L = 1.5\Omega$ , $V_{GS} = -10\text{V}$		16	30	ns
Turn-On Rise Time	$t_r$			18	35	
Turn-Off Delay Time	$t_{d(off)}$			39	78	
Turn-Off Fall Time	$t_f$			44	87	
<b>Drain-Source Diode Characteristics</b> <sup>*5</sup>						
Maximum Body-Diode Continuous Current	$I_S$				-50	A
Maximum Body-Diode Pulsed Current	$I_{SM}$				-200	
Diode Forward Voltage	$V_{SD}$	$I_S = -20\text{A}$ , $V_{GS} = 0\text{V}$			-1.2	V

Notes: 5. Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

6. Guaranteed by design, not subject to production.

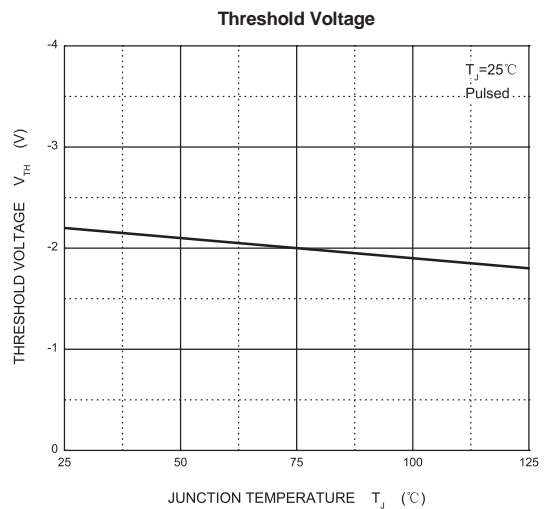
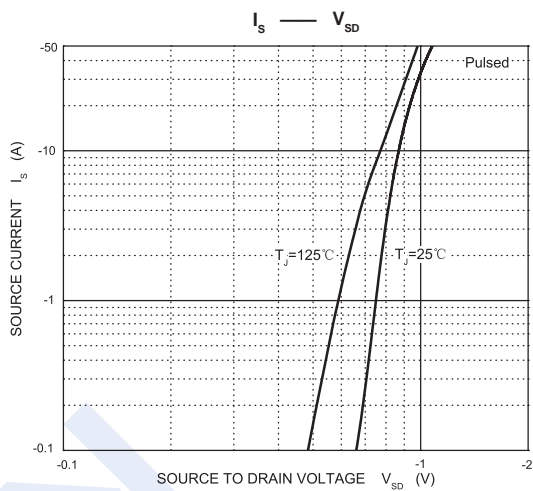
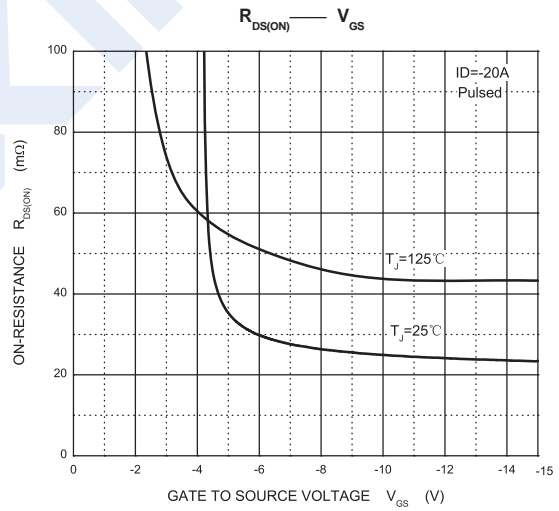
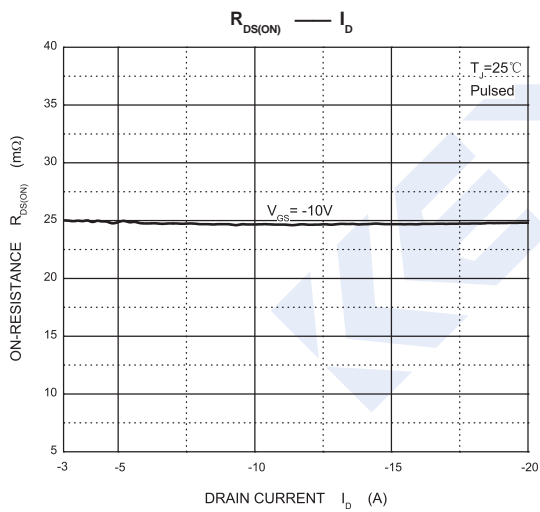
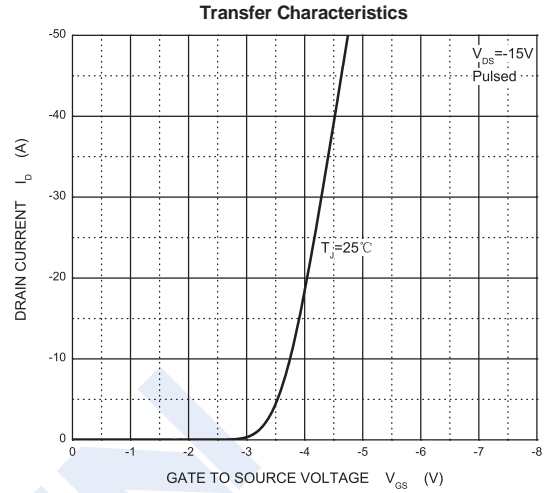
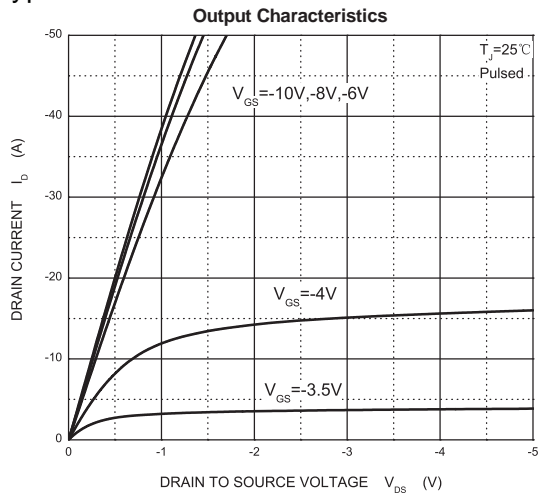
## ■ Marking

NDT50P06	50P06 K***
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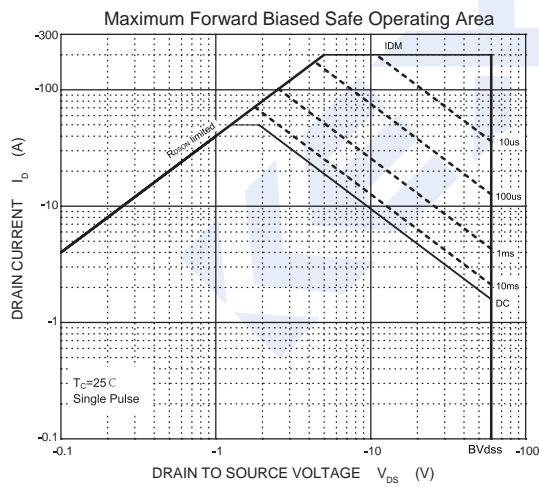
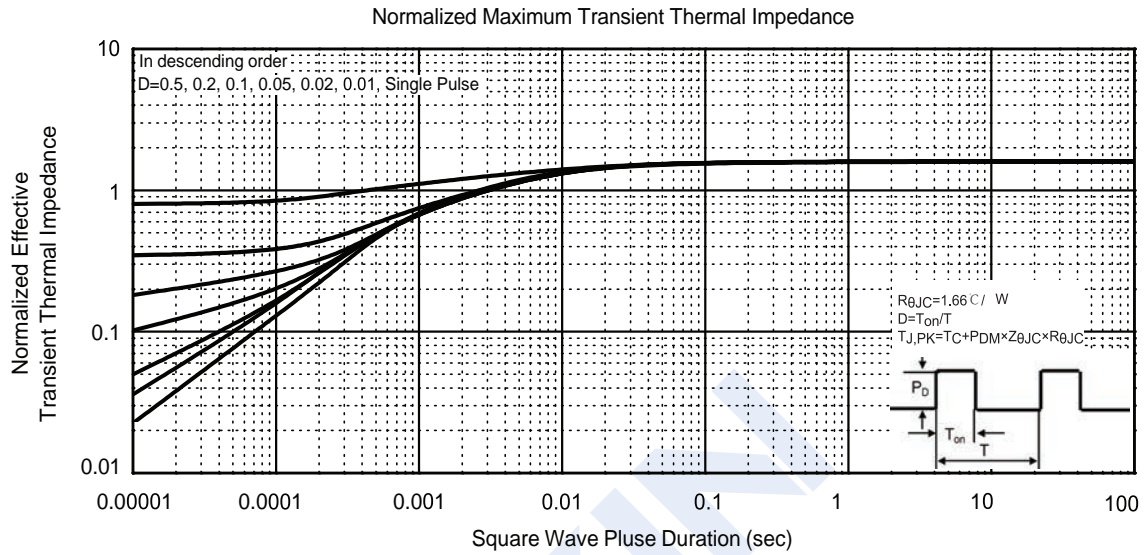
# P-Channel MOSFET

## NDT50P06

### Typical Characteristics



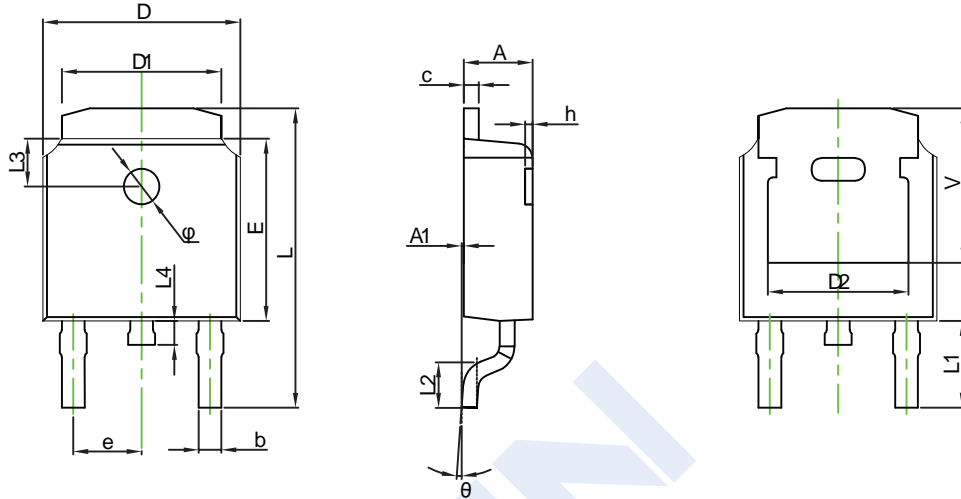
## P-Channel MOSFET NDT50P06



## P-Channel MOSFET

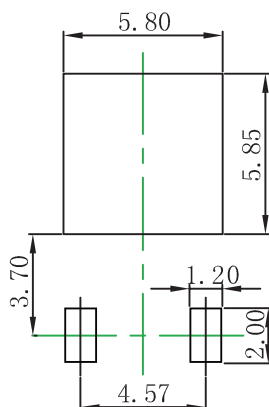
## NDT50P06

## ■ TO-252 Package Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	

## ■ TO-252 Suggested Pad Layout



## Note:

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.05\text{mm}$ .
3. The pad layout is for reference purposes only.