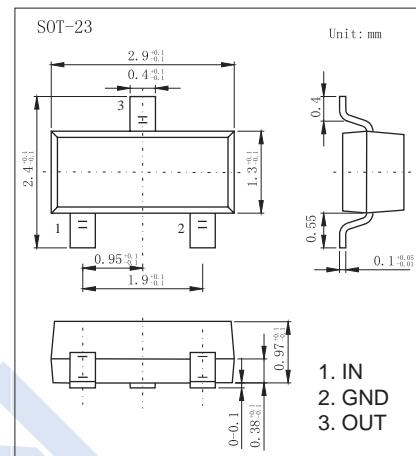
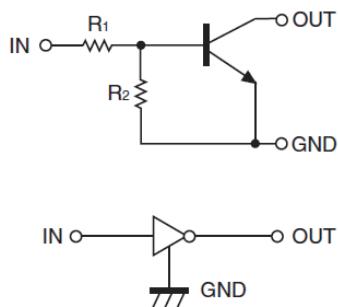


Digital Transistors

KTC105

■ Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Supply Voltage	Vcc	50	V
Input Voltage	Vin	-5~+12	
Output Current	Io	100	mA
Power Dissipation	Pd	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature range	Tstg	-55 to 150	

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input voltage	Vi(off)	Vcc= 5 V , Io=100 uA			0.5	V
	Vi(on)	Vo= 0.3 V , Io= 5 mA	1.1			
Output voltage	Vo(on)	Io= 5 mA , Ii=0.25mA			0.3	
Input current	Ii	Vi= 5 V			3.6	mA
Output current	Io(off)	Vcc= 50 V , Vi=0			0.5	uA
DC current gain	Gi	Vo=5V, Io=10mA	80			
Input resistance	R1		1.54	2.2	2.86	KΩ
Resistance ratio	R2/R1		17	21	26	
Transition frequency	ft	Vo= 10V, Io= -5 mA, f=100MHz		250		MHz

■ Marking

Marking	E42
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Digital Transistors**KTC105**

■ Typical Characteristics

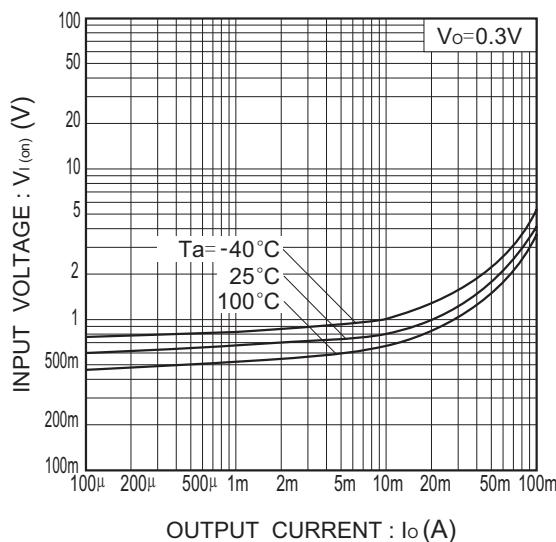
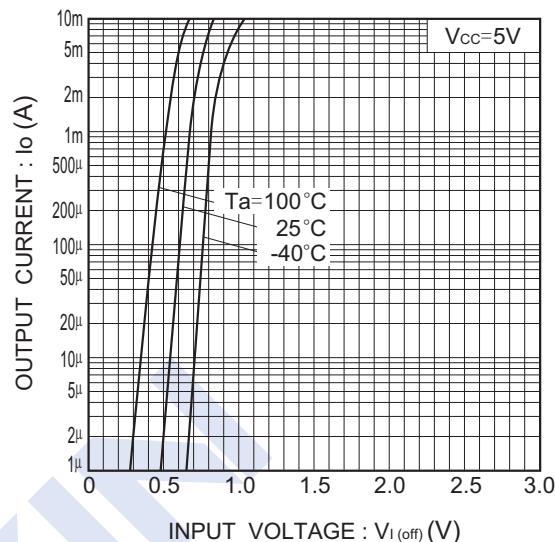
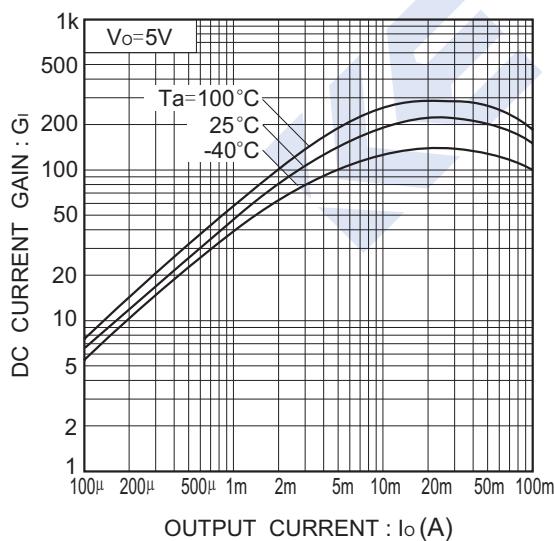
Fig.1 Input voltage vs. output current
(ON characteristics)Fig.2 Output current vs. input voltage
(OFF characteristics)

Fig.3 DC current gain vs. output current

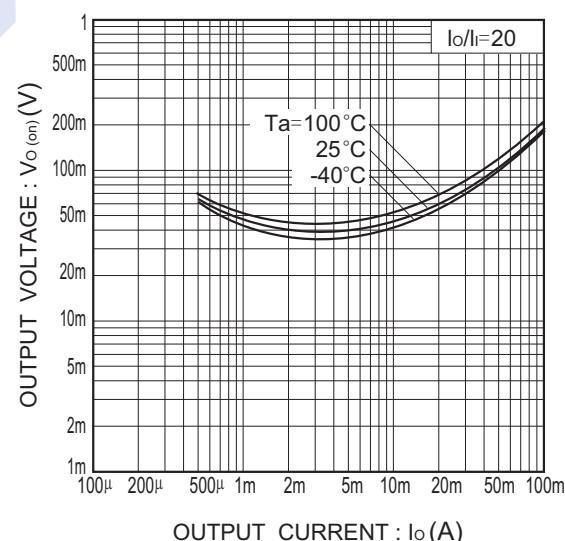


Fig.4 Output voltage vs. output current