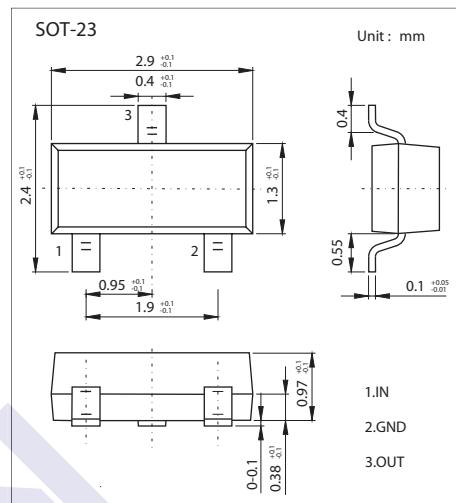
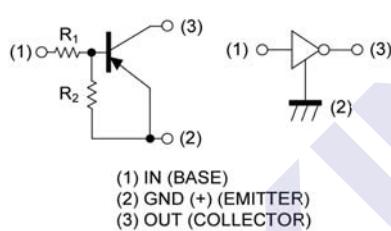


Digital Transistors

KTA207

■ Features

- Built-In Biasing Resistors, $R_1 = 22\text{k}\Omega$, $R_2 = 47\text{k}\Omega$
 - Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit) .
 - Only the on/off conditions need to be set for operation, making the circuit design easy.
 - Marking:35



■ Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	-50	V
Input voltage	V _{IN}	-40 to 10	
Output current	I _O	-50	mA
Collector current (Note 1)	I _C (Max.)	-100	
Power dissipation (Note 2)	P _D	200	mW
Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55 to 150	

■ Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =-5V, I _O =-100µA			-0.4	V
	V _{I(on)}	V _O =-0.3V, I _O =-2mA	-2.5			
Output voltage	V _{O(on)}	I _O =-10mA, I _I =-0.5mA		-100	-300	mV
Input current	I _I	V _I =-5V			-360	µA
Output current	I _{O(off)}	V _{CC} =-50V, V _I =0V			-500	nA
DC current gain	G _I	V _O =-5V, I _O =-5mA	68			
Input resistance	R _I		15.4	22	28.6	kΩ
Resistance ratio	R ₂ /R ₁		1.7	2.1	2.6	
Transition frequency (Note 1)	f _T	V _{CE} =-10V, I _E = 5mA, f=100MHz		250		MHz

Note 1: Characteristics of built-in transistor

Note 2: Each terminal mounted on a reference land.

Digital Transistors

KTA207

■ Typical Characteristics

Fig.1 Input voltage vs. output current (ON characteristics)

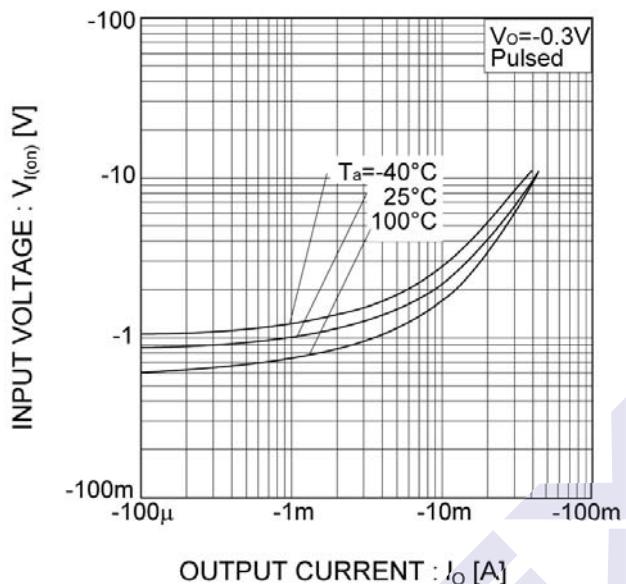


Fig.2 Output current vs. input voltage (OFF characteristics)

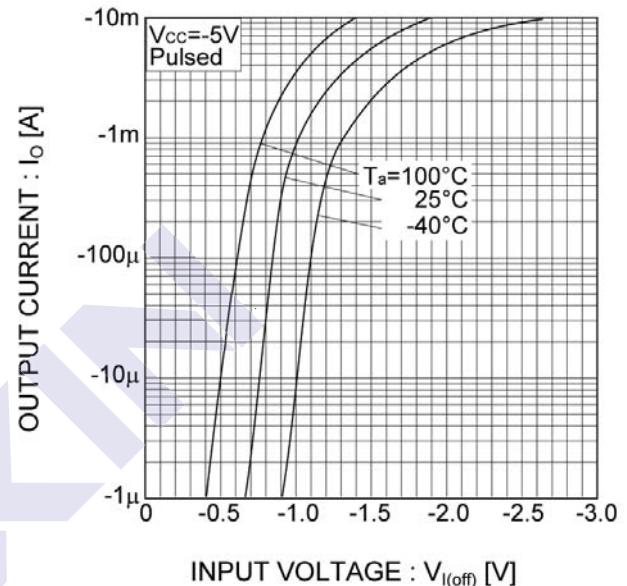


Fig.3 Output current vs. output voltage

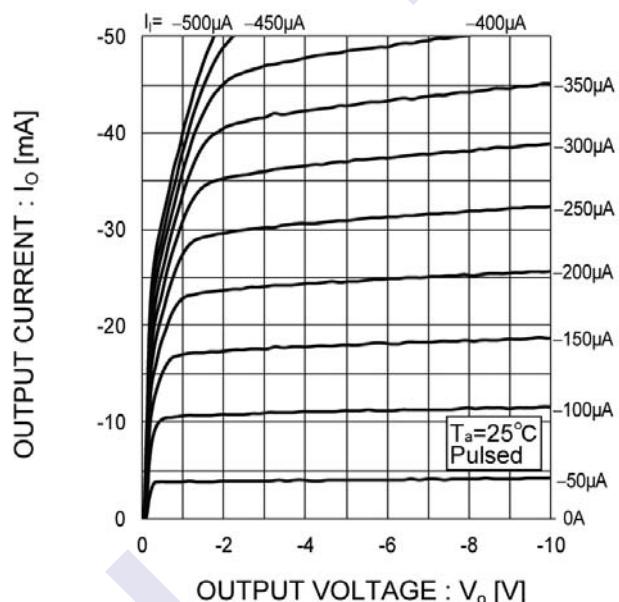


Fig.4 DC current gain vs. output current

