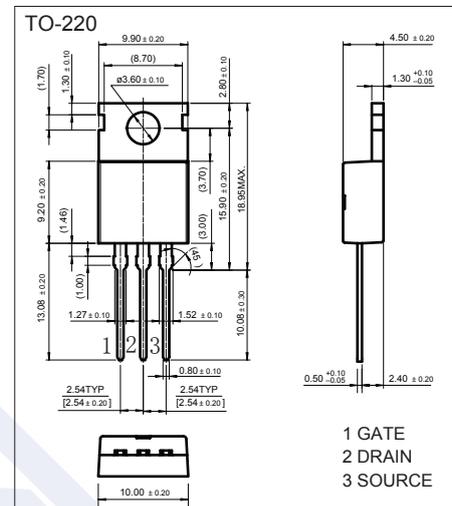
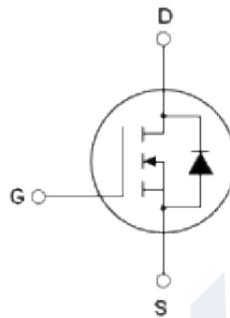


N-Channel MOSFET

KI20N65

■ Features

- V_{DS} (V) = 650V
- I_D = 20A
- $R_{DS(ON)} < 190m\Omega$ @ $V_{GS}=10V$

■ Absolute Maximum Ratings ($T_c = 25^\circ\text{C}$)

| Parameter | Symbol | Rating | Unit |
|--|------------|---------------------------|---------------------------|
| Drain-Source Voltage | V_{DS} | 650 | V |
| Gate-Source Voltage | V_{GS} | ± 30 | V |
| Continuous Drain Current | I_D | $T_c = 25^\circ\text{C}$ | 20 |
| | | $T_c = 100^\circ\text{C}$ | 12.5 |
| Pulsed Drain Current (Note 1) | I_{DM} | 80 | A |
| Avalanche Current (Note 1) | I_{AR} | 10.5 | A |
| Single Pulse Avalanche Energy (Note 2) | E_{AS} | 441 | mJ |
| Repetitive Avalanche energy, t_{AR} limited by T_{jmax} (Note 1) | E_{AR} | 0.7 | mJ |
| Drain Source voltage slope, $V_{DS} \leq 480$ V | dv/dt | 50 | V/ns |
| Reverse diode dv/dt , $V_{DS} \leq 480$ V, $I_{SD} < I_D$ | dv/dt | 15 | V/ns |
| Power Dissipation ($T_c = 25^\circ\text{C}$) | P_D | 188 | W |
| Derate above 25°C | | 1.5 | W/ $^\circ\text{C}$ |
| Thermal Resistance, Junction- to-Case | R_{thJC} | 0.66 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction- to-Ambient | R_{thJA} | 62.5 | $^\circ\text{C}/\text{W}$ |
| Junction Temperature | T_J | 150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{stg} | -55 to 150 | $^\circ\text{C}$ |

Notes: 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. $T_j=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$

N-Channel MOSFET

KI20N65

■ Electrical Characteristics ($T_A = 25^\circ\text{C}$, unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|-----------------------------------|--------------|--|-----|------|-----------|---------------|
| Drain-Source Breakdown Voltage | V_{DS} | $I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$ | 650 | | | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=650\text{V}$, $V_{GS}=0\text{V}$, $T_C=25^\circ\text{C}$ | | | 1 | μA |
| | | $V_{DS}=650\text{V}$, $V_{GS}=0\text{V}$, $T_C=125^\circ\text{C}$ | | | 100 | |
| Gate-Body Leakage Current | I_{GSS} | $V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$ | | | ± 100 | nA |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$ | 2 | | 4 | V |
| Static Drain-Source On-Resistance | $R_{DS(on)}$ | $V_{GS}=10\text{V}$, $I_D=10\text{A}$ | | | 190 | m Ω |
| Forward Transconductance | g_{FS} | $V_{DS}=20\text{V}$, $I_D=10\text{A}$ | | 16 | | S |
| Input Capacitance | C_{iss} | $V_{GS}=0\text{V}$, $V_{DS}=50\text{V}$, $f=1\text{MHz}$ | | 2250 | | μF |
| Output Capacitance | C_{oss} | | | 83 | | |
| Reverse Transfer Capacitance | C_{rss} | | | 1.6 | | |
| Total Gate Charge | Q_g | $V_{DS}=480\text{V}$, $I_D=20\text{A}$, $V_{GS}=10\text{V}$ | | 36 | | nC |
| Gate Source Charge | Q_{gs} | | | 14 | | |
| Gate Drain Charge | Q_{gd} | | | 8.5 | | |
| Turn-On DelayTime | $t_{d(on)}$ | $V_{DS}=380\text{V}$, $I_D=11\text{A}$, $V_{GS}=10\text{V}$, $R_G=4\Omega$ | | 11 | | ns |
| Turn-On Rise Time | t_r | | | 6 | | |
| Turn-Off DelayTime | $t_{d(off)}$ | | | 61 | | |
| Turn-Off Fall Time | t_f | | | 4.5 | | |
| Body-Diode Continuous Current | I_S | $T_C=25^\circ\text{C}$ | | | 20 | A |
| Body-Diode Pulsed Current | I_{SM} | | | | 80 | |
| Diode Forward Voltage (Note 1) | V_{SD} | $T_J=25^\circ\text{C}$, $I_{SD}=20\text{A}$, $V_{GS}=0\text{V}$ | | | 1.3 | V |
| Reverse Recovery Time (Note 1) | t_{rr} | $T_J=25^\circ\text{C}$, $I_F=20\text{A}$, $di/dt=100\text{A}/\mu\text{s}$ | | 310 | | nS |
| Reverse Recovery Charge (Note 1) | Q_{rr} | | | 5 | | nC |
| Peak Reverse Recovery Current | I_{rrm} | | | 28 | | A |

N-Channel MOSFET

KI20N65

■ Typical Electrical And Thermal Characteristics

Figure1. Safe operating area

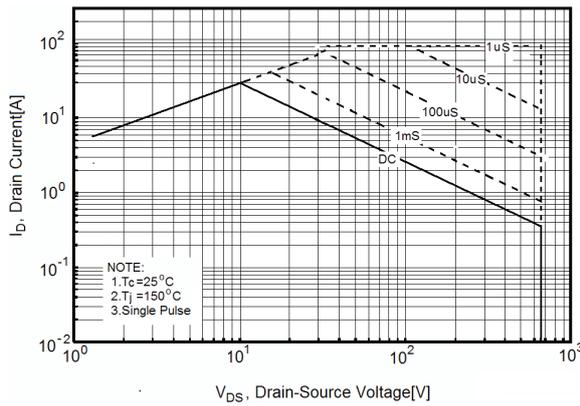


Figure2. Capacitance

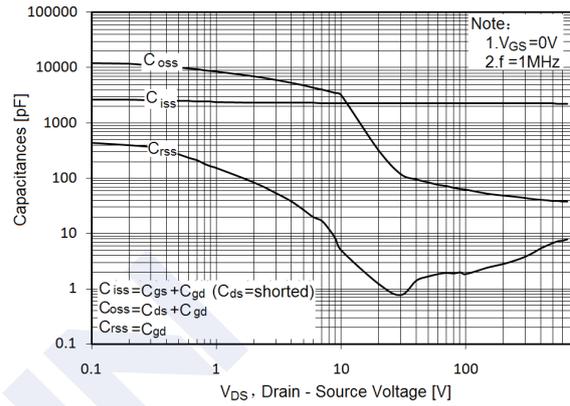


Figure3. Source-Drain Diode Forward Voltage

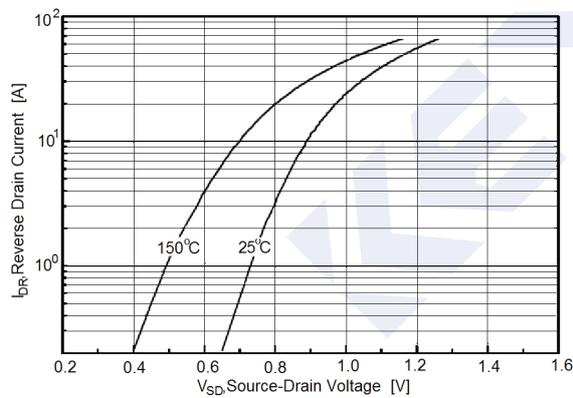


Figure4. Output characteristics

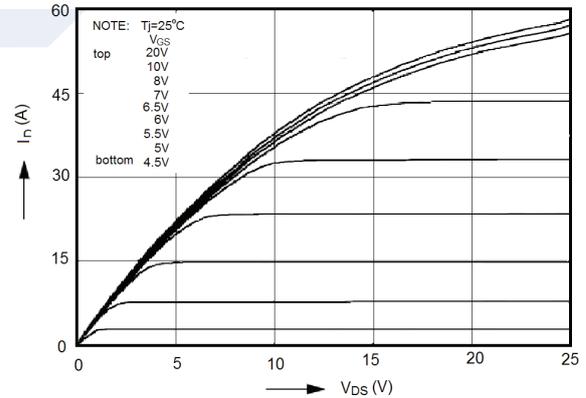


Figure5. Transfer characteristics

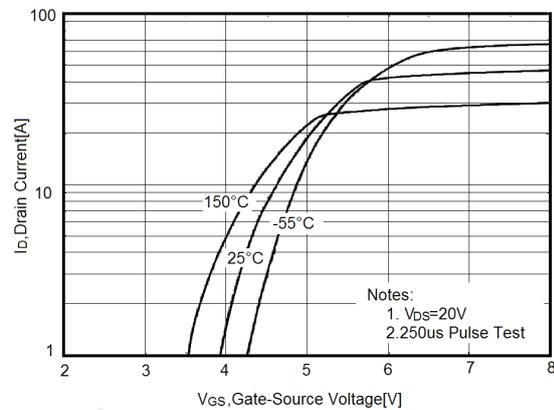
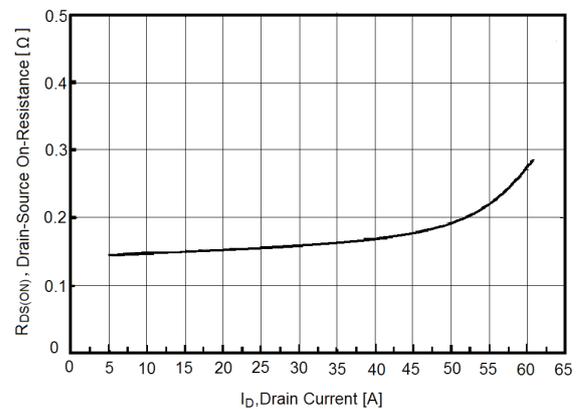


Figure6. Static drain-source on resistance



N-Channel MOSFET

KI20N65

Figure7. $R_{DS(ON)}$ vs Junction Temperature

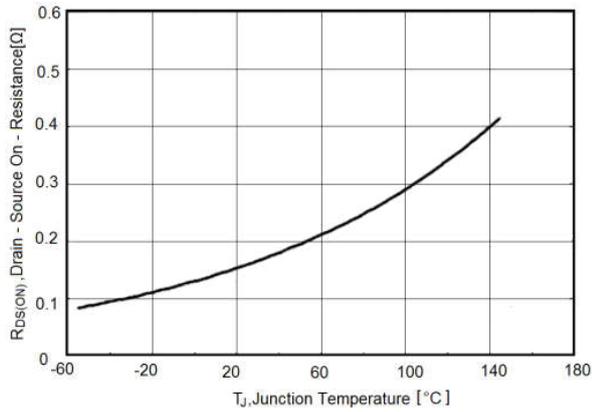


Figure8. BV_{DSS} vs Junction Temperature

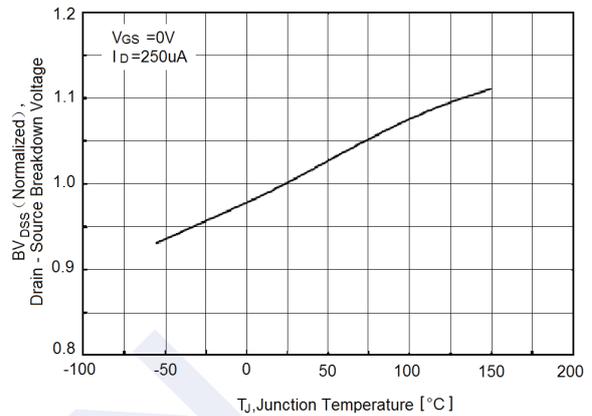


Figure9. Maximum I_D vs Junction Temperature

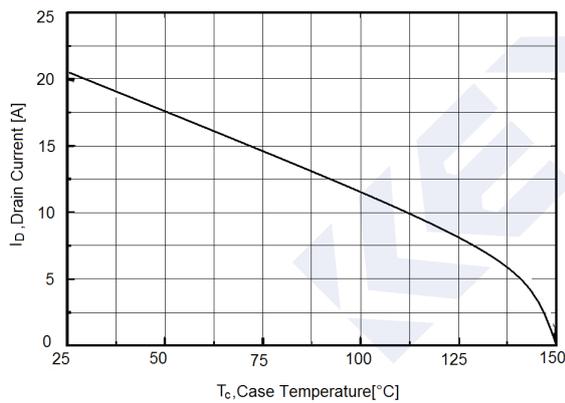


Figure10. Transient Thermal Impedance

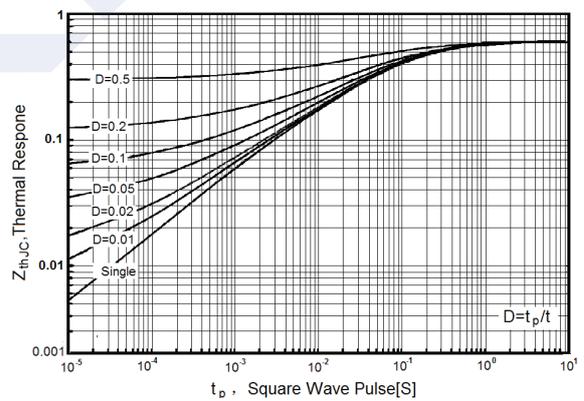
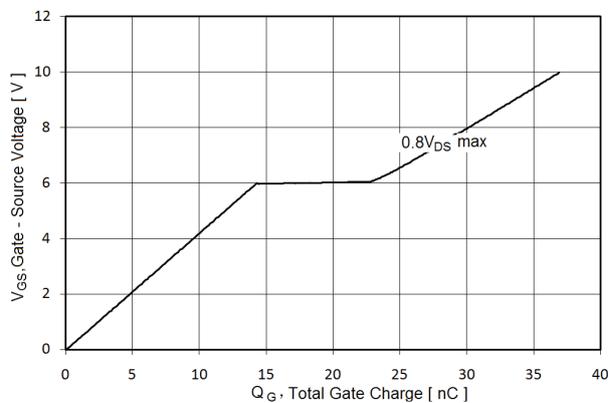


Figure11. Gate charge waveforms

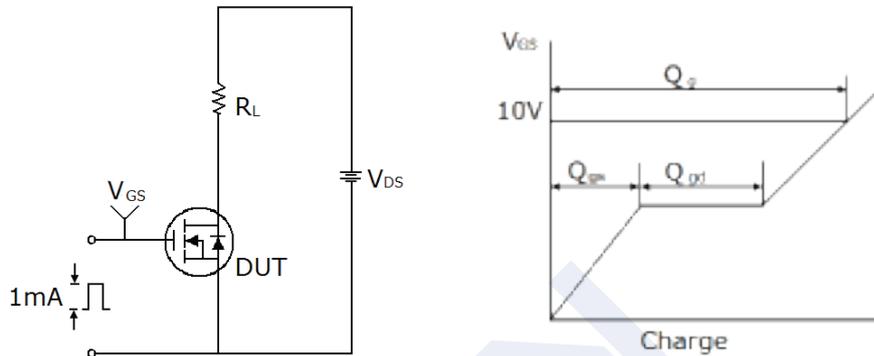


N-Channel MOSFET

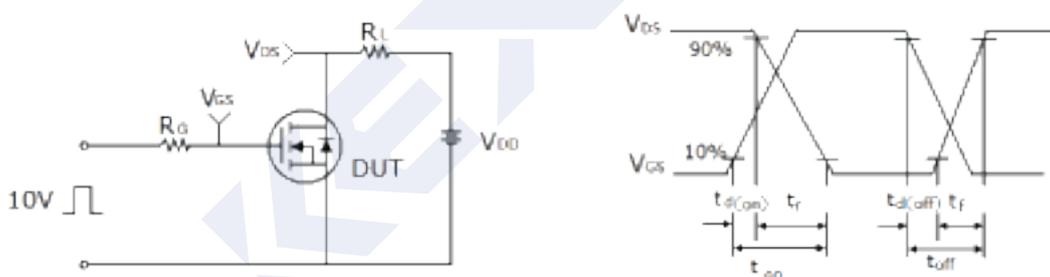
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■ Test Circuit

1) Gate charge test circuit & Waveform



2) Switch Time Test Circuit:



3) Unclamped Inductive Switching Test Circuit & Waveforms

