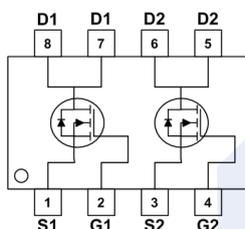
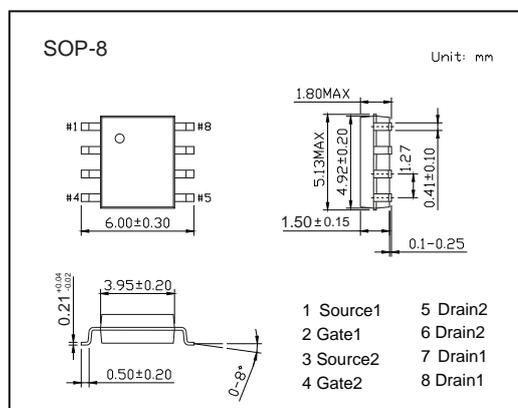


Dual N-Channel MOSFET

2KK5134

■ Features

- $BV_{DSS} = 60\text{ V}$
- $I_D = 6.5\text{ A}$
- $R_{DS(ON)} T_{yp.} = 38\text{ m}\Omega$ (at $V_{GS} = 10\text{ V}$)
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode

■ Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D	$T_C = 25^\circ\text{C}$	16.2
		$T_A = 25^\circ\text{C}$	6.5
Pulsed Drain Current ^{*1}	I_{DM}	20	A
Avalanche energy $L=0.1\text{mH}$ ^{*1}	EAS, EAR	10	mJ
Power Dissipation	P_D	$T_C = 25^\circ\text{C}$	31.3
		$T_A = 25^\circ\text{C}$	3
Thermal Resistance, Junction- to-Ambient	$R_{\theta JA}$	42	$^\circ\text{C/W}$
Thermal Resistance, Junction- to-Case	$R_{\theta JC}$	4	
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to 150	

Note 1. Repetitive rating; pulse width limited by max. junction temperature.

Dual N-Channel MOSFET

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■ Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\ \mu\text{A}$, $V_{GS} = 0\text{V}$	60			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60\text{V}$, $V_{GS} = 0\text{V}$			5	μA
		$V_{DS} = 48\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 125^\circ\text{C}$			100	
Gate to Source Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$			± 100	nA
Gate to Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	1		3	V
Static Drain-Source On-Resistance ^{*2}	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 5\text{A}$		38	50	m Ω
		$V_{GS} = 4.5\text{V}$, $I_D = 5\text{A}$		45	60	
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 30\text{V}$, $f = 1\text{MHz}$		300		pF
Output Capacitance	C_{oss}			18		
Reverse Transfer Capacitance	C_{rss}			52		
Gate Resistance	R_g	$V_{GS} = 0\text{V}$, $f = 1\text{MHz}$		3.1		Ω
Total Gate Charge	Q_g	$V_{GS} = 10\text{V}$, $V_{DS} = 30\text{V}$, $I_D = 5\text{A}$		6		nC
Gate Source Charge	Q_{gs}			1.2		
Gate Drain Charge	Q_{gd}			1.1		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}$, $V_{DD} = 30\text{V}$, $R_G = 6\Omega$, $I_D = 5\text{A}$		2		ns
Turn-On Rise Time	t_r			5.6		
Turn-Off Delay Time	$t_{d(off)}$			23		
Turn-Off Fall Time	t_f			14		
Body Diode Reverse Recovery Time	t_{rr}			29		
Body Diode Reverse Recovery Charge	Q_{rr}	$I_S = 5\text{A}$, $V_{GS} = 0\text{V}$, $di/dt = 100\text{A}/\mu\text{s}$		24		nC
Continuous Source Current	I_{SD}				2.5	A
Pulsed Source Current	I_{SM}				10	
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}$, $I_S = 5\text{A}$		0.85	1.2	V

Note 2. Pulse width $\leq 300\ \mu\text{s}$; duty cycle $\leq 2\%$.

■ Marking

Marking	K5134 KA****
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Dual N-Channel MOSFET

2KK5134

■ Typical Characteristics

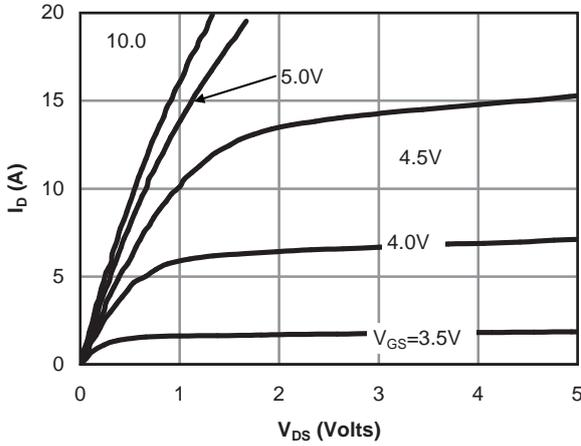


Fig 1: On-Region Characteristics

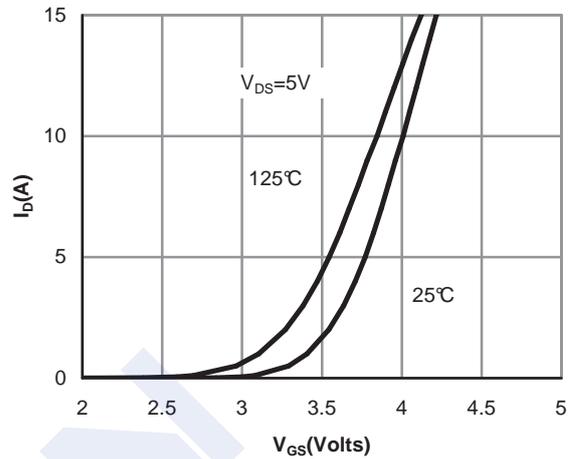


Figure 2: Transfer Characteristics

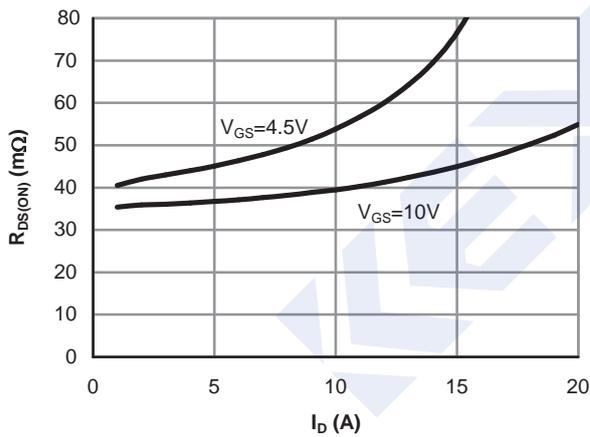


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

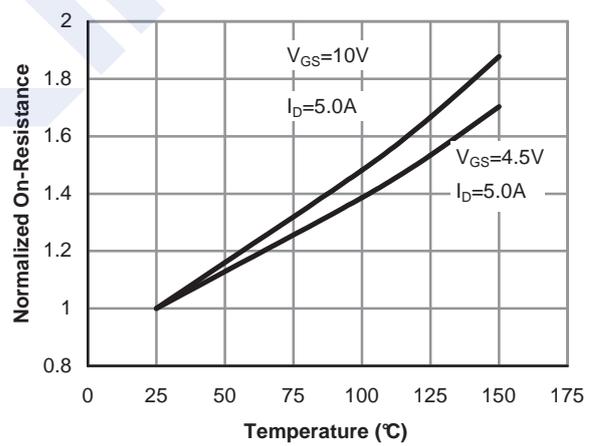


Figure 4: On-Resistance vs. Junction Temperature

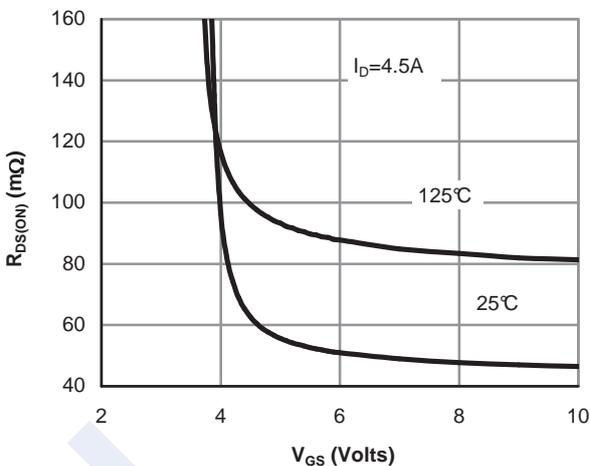


Figure 5: On-Resistance vs. Gate-Source Voltage

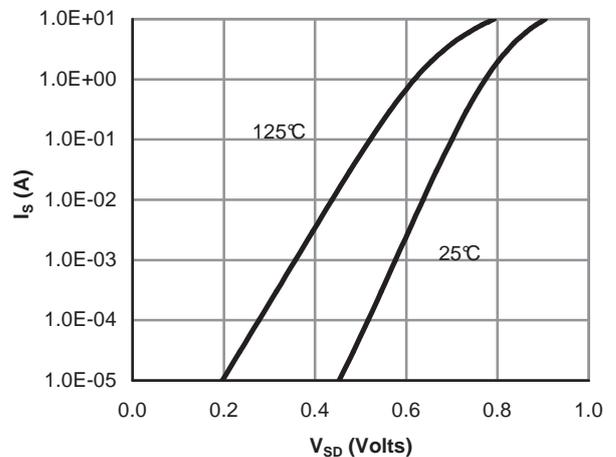


Figure 6: Body-Diode Characteristics

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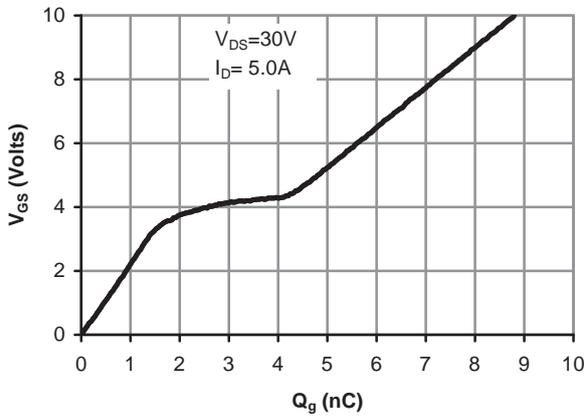


Figure 7: Gate-Charge Characteristics

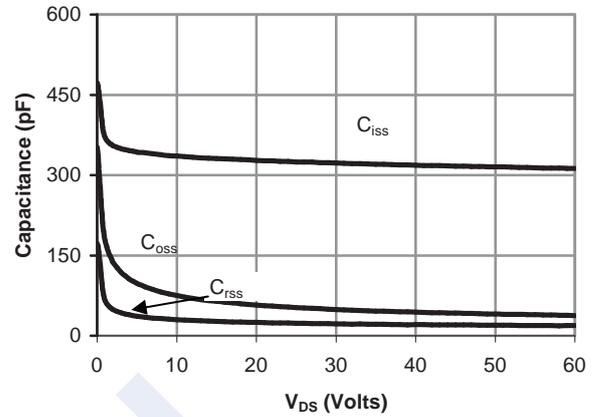


Figure 8: Capacitance Characteristics

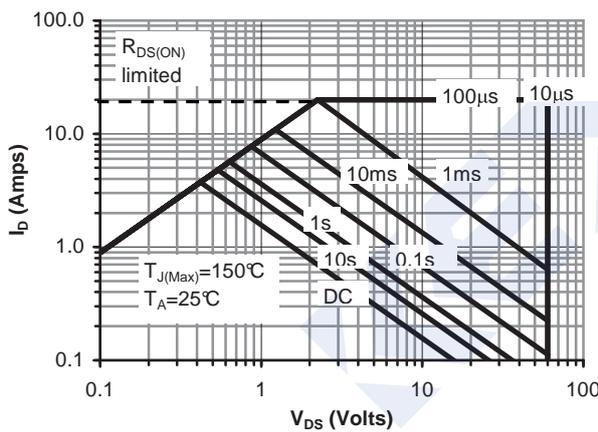


Figure 9: Maximum Forward Biased Safe Operating Area

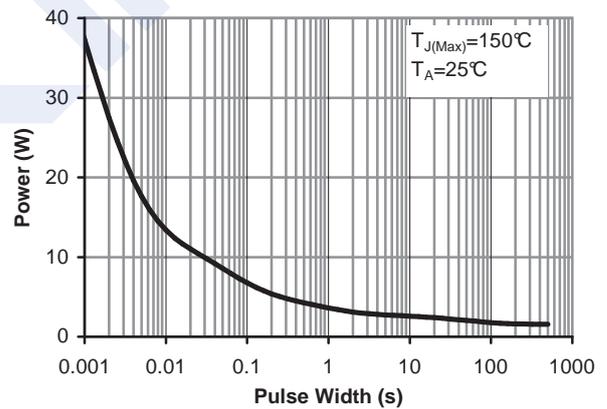


Figure 10: Single Pulse Power Rating Junction-to-Ambient

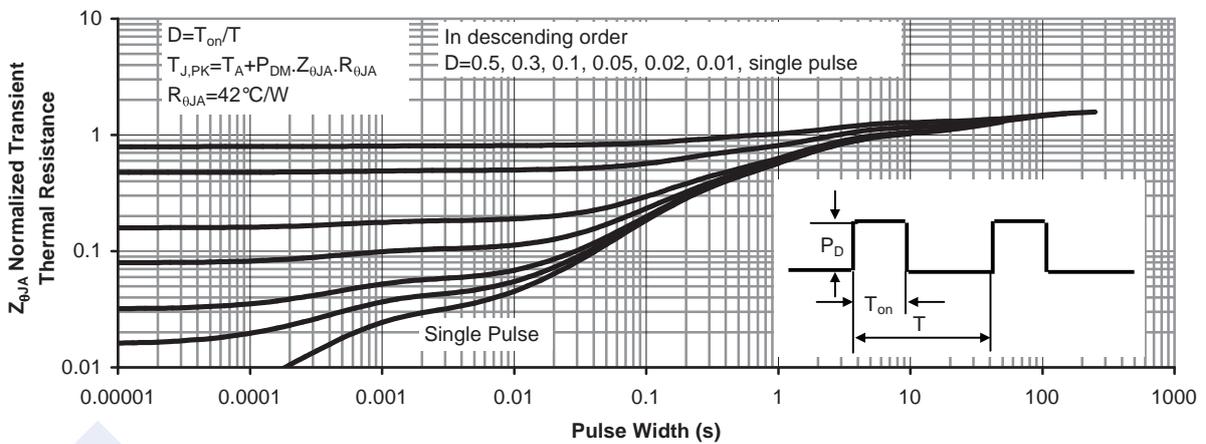


Figure 11: Normalized Maximum Transient Thermal Impedance