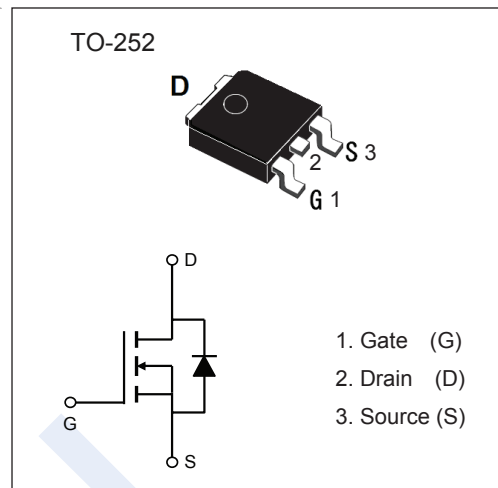


N-Channel MOSFET

2KK5092

■ Features

- $V_{DS} = 40\text{ V}$
- I_D (at $V_{GS}=10\text{V}$) = 70 A
- $R_{DS(ON)}$ (at $V_{GS} = 10\text{ V}$) < 3.0 m Ω
- $R_{DS(ON)}$ (at $V_{GS} = 4.5\text{ V}$) < 3.8 m Ω
- 100% UIS Tested
- 100% R_g Tested

■ Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	40	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	$T_C = 25^\circ\text{C}$	70	A
		$T_C = 100^\circ\text{C}$	55	
Pulsed Drain Current (Note 2)	I_{DM}	300		
Continuous Drain Current	I_{DSM}	$T_A = 25^\circ\text{C}$	23	
		$T_A = 70^\circ\text{C}$	18	
Avalanche Current (Note 2)	I_{AS}	68	A	
Avalanche Energy $L = 0.1\text{mH}$ (Note 2)	E_{AS}	231	mJ	
Thermal Resistance, Junction- to-Ambient (Note 5)	$R_{\theta JA}$	47	$^\circ\text{C}/\text{W}$	
Thermal Resistance, Junction- to-Case	$R_{\theta JC}$	1		
Power Dissipation (Note 4)	P_D	$T_C = 25^\circ\text{C}$	150	W
		$T_C = 100^\circ\text{C}$	75	
Power Dissipation (Note 5)	P_{DSM}	$T_A = 25^\circ\text{C}$	2.7	
		$T_A = 70^\circ\text{C}$	1.7	
Junction Temperature	T_J	175	$^\circ\text{C}$	
Storage Temperature Range	T_{stg}	-55 to 175		

Notes:

1. The maximum current rating is package limited.
2. Single pulse width limited by junction temperature $T_{J(MAX)}=175^\circ\text{C}$.
3. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
4. The power dissipation P_D is based on $T_{J(MAX)}=175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
5. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} t \leq 10\text{s}$ and the maximum allowed junction temperature of 175°C . The value in any given application depends on the user's specific board design.

N-Channel MOSFET

2KK5092

■ Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\ \mu\text{A}$, $V_{GS} = 0\text{V}$	40			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 40\text{V}$, $V_{GS} = 0\text{V}$			1	μA	
		$V_{DS} = 40\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 55^\circ\text{C}$			5		
Gate to Source Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 20\text{V}$			± 100	nA	
Gate to Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$	1.0		2.2	V	
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$		2.3	3.0	m Ω	
		$V_{GS} = 10\text{V}$, $I_D = 20\text{A}$, $T_J = 125^\circ\text{C}$		3.6	4.8		
		$V_{GS} = 4.5\text{V}$, $I_D = 20\text{A}$		2.8	3.8		
Forward Transconductance	g_{FS}	$V_{DS} = 5\text{V}$, $I_D = 20\text{A}$		78		S	
Dynamic Characteristics							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = 20\text{V}$, $f = 1\text{MHz}$		3510	4300	pF	
Output Capacitance	C_{oss}			1070			
Reverse Transfer Capacitance	C_{rss}			68			
Gate Resistance	R_g	$V_{GS} = 0\text{V}$, $V_{DS} = 0\text{V}$, $f = 1\text{MHz}$	0.5	1	1.5	Ω	
Switching Characteristics							
Total Gate Charge (10V)	Q_g	$V_{GS} = 10\text{V}$, $V_{DS} = 20\text{V}$, $I_D = 20\text{A}$		49	60	nC	
Total Gate Charge (4.5V)				22			
Gate Source Charge			Q_{gs}		9		
Gate Drain Charge			Q_{gd}		7		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 20\text{V}$, $R_L = 1\ \Omega$, $R_{GEN} = 3\ \Omega$		11		ns	
Turn-On Rise Time	t_r			10			
Turn-Off Delay Time	$t_{d(off)}$			38			
Turn-Off Fall Time	t_f			11			
Drain-Source Diode Characteristics							
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}$, $di/dt = 500\text{A}/\mu\text{s}$		21		ns	
Body Diode Reverse Recovery Charge	Q_{rr}			58		nC	
Maximum Body-Diode Continuous Current	I_S	(Note 1)			70	A	
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}$, $I_S = 1\text{A}$		0.65	1	V	

Notes:

- The static characteristics in Figures 1 to 6 are obtained using $<300\ \mu\text{s}$ pulses, duty cycle 0.5% max.
- These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)} = 175^\circ\text{C}$. The SOA curve provides a single pulse rating.
- These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

■ Marking

Marking	K5092 KC***
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N-Channel MOSFET

2KK5092

Typical Electrical And Thermal Characteristics

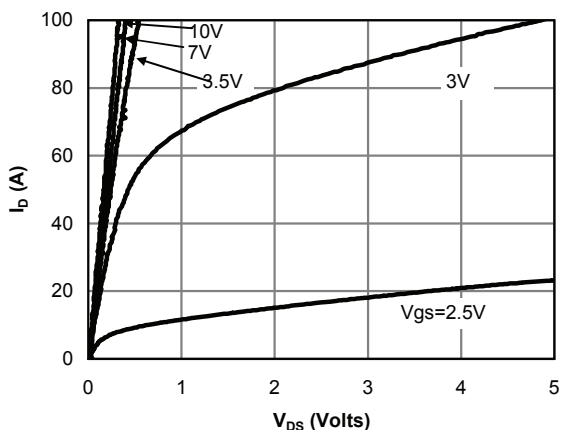


Fig 1: On-Region Characteristics (Note 6)

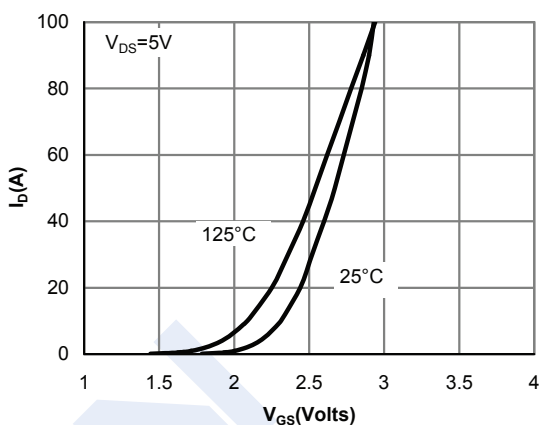


Figure 2: Transfer Characteristics (Note 6)

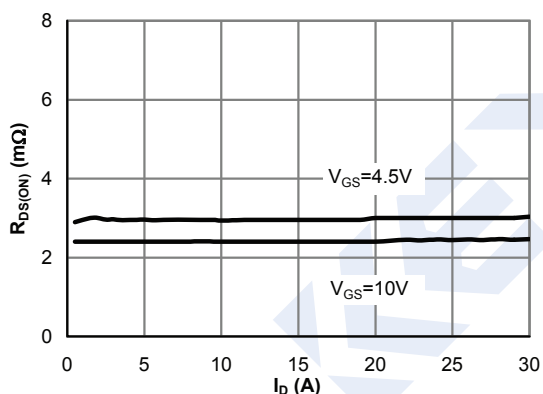


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note 6)

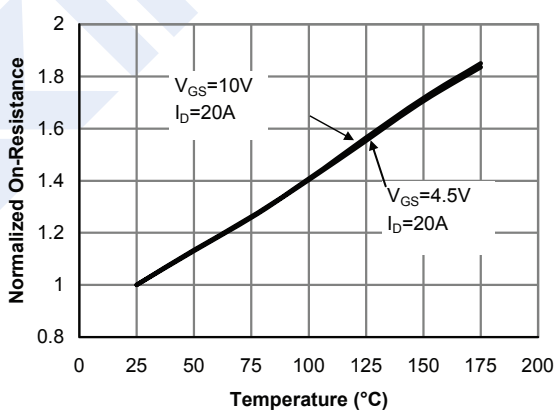


Figure 4: On-Resistance vs. Junction Temperature (Note 6)

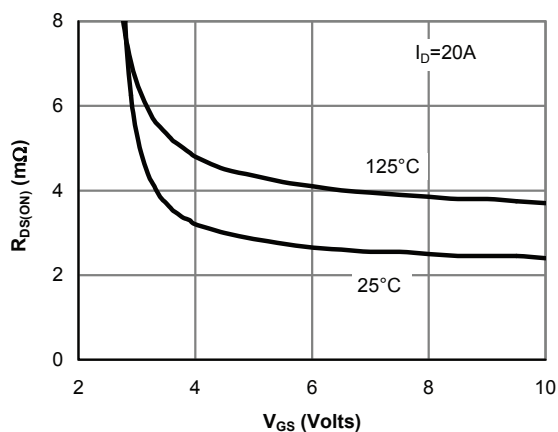


Figure 5: On-Resistance vs. Gate-Source Voltage (Note 6)

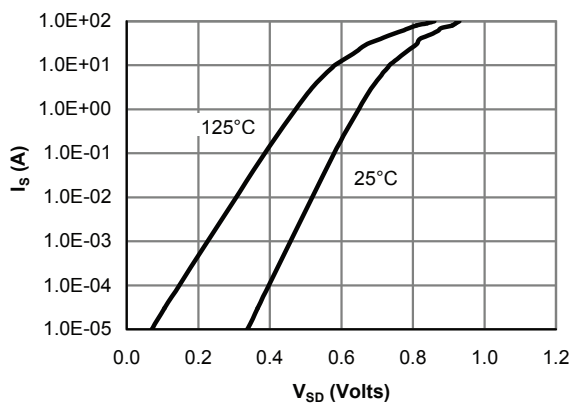


Figure 6: Body-Diode Characteristics (Note 6)

N-Channel MOSFET

2KK5092

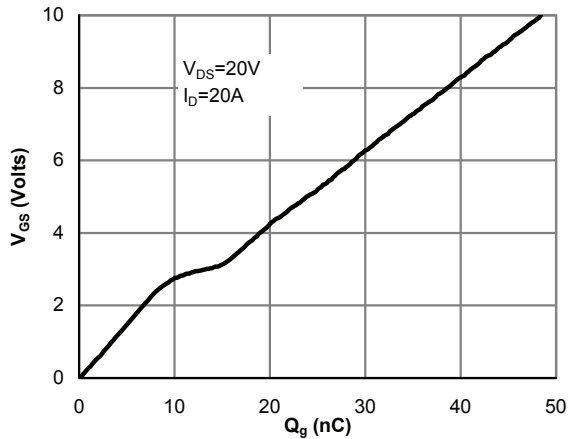


Figure 7: Gate-Charge Characteristics

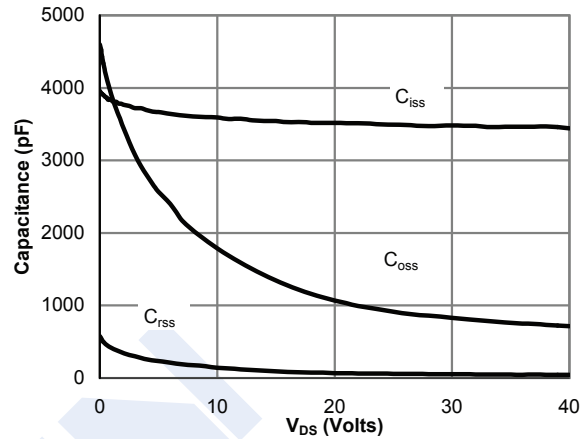


Figure 8: Capacitance Characteristics

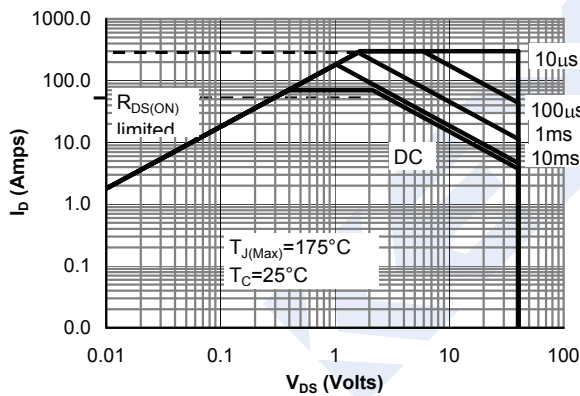


Figure 9: Maximum Forward Biased Safe Operating Area (Note 7)

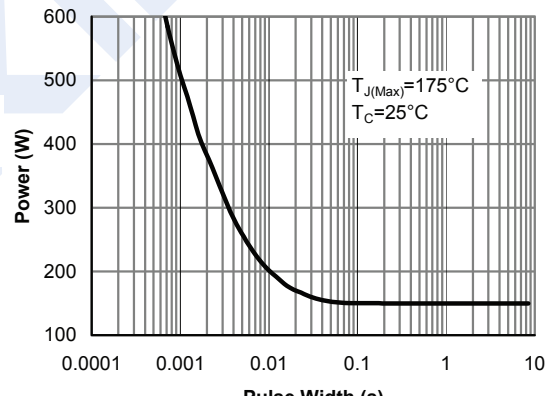


Figure 10: Single Pulse Power Rating Junction-to-Case (Note 7)

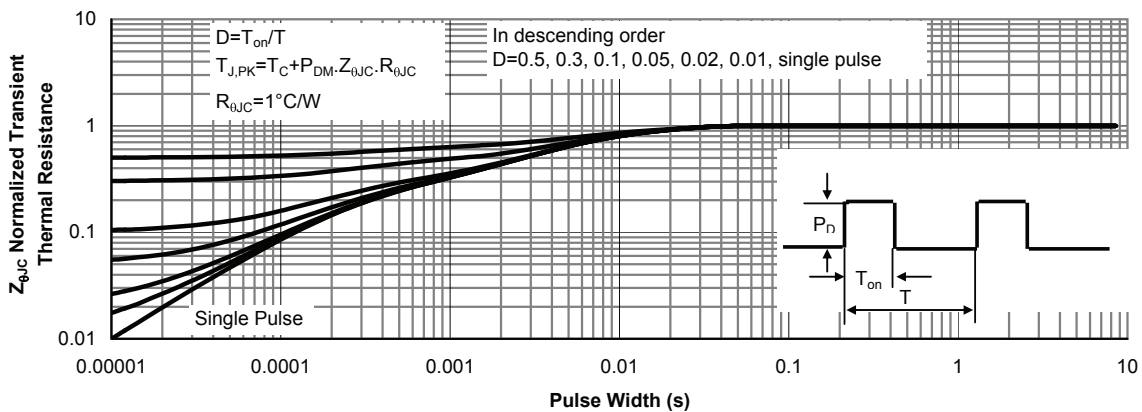


Figure 11: Normalized Maximum Transient Thermal Impedance (Note 7)

N-Channel MOSFET

2KK5092

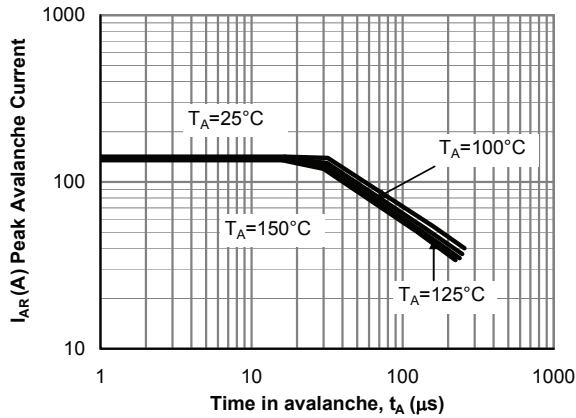


Figure 12: Single Pulse Avalanche capability

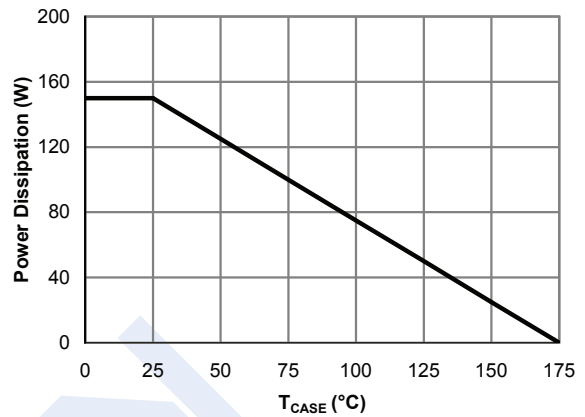


Figure 13: Power De-rating (Note 7)

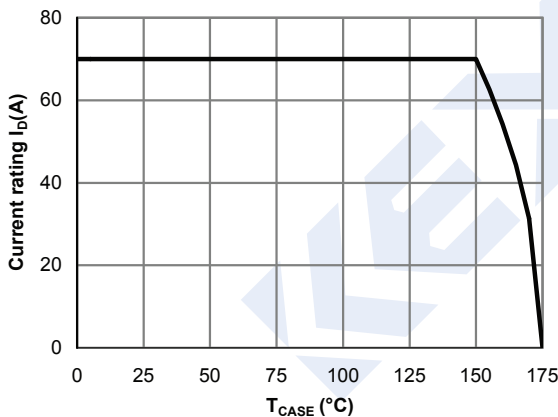


Figure 14: Current De-rating (Note 7)

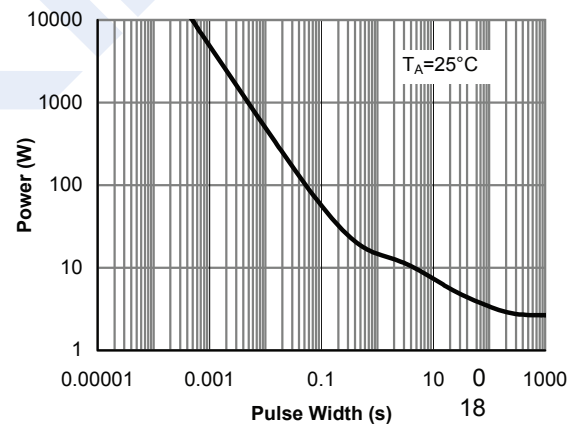


Figure 15: Single Pulse Power Rating Junction-to-Ambient (Note 8)

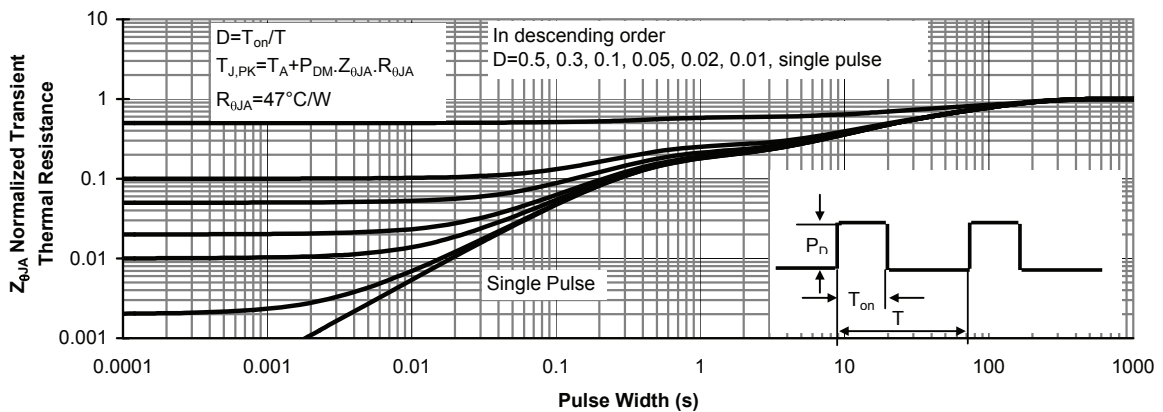
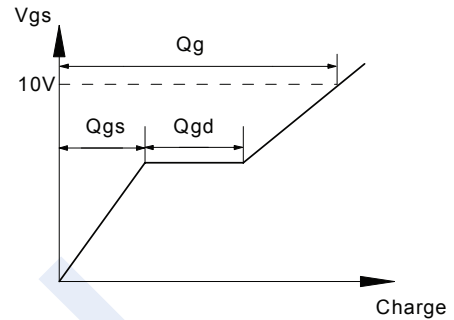
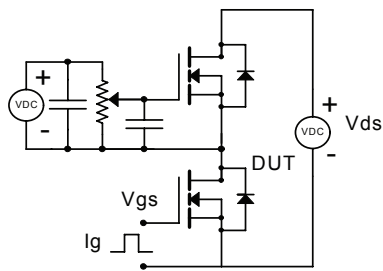


Figure 16: Normalized Maximum Transient Thermal Impedance (Note 8)

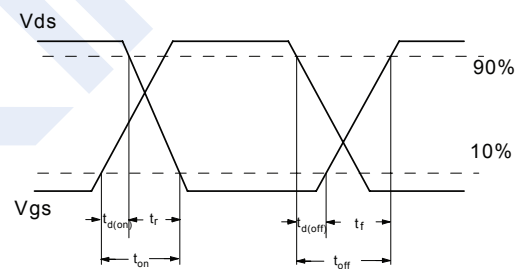
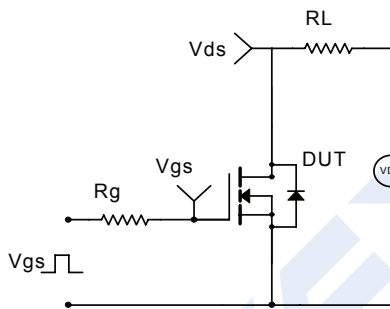
N-Channel MOSFET

2KK5092

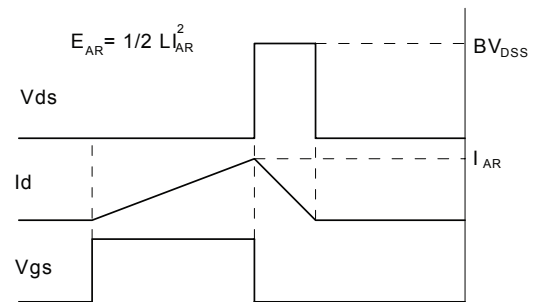
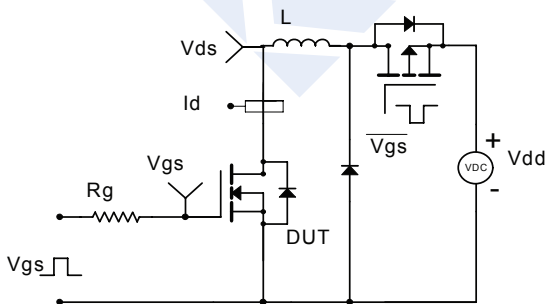
Gate Charge Test Circuit & Waveform



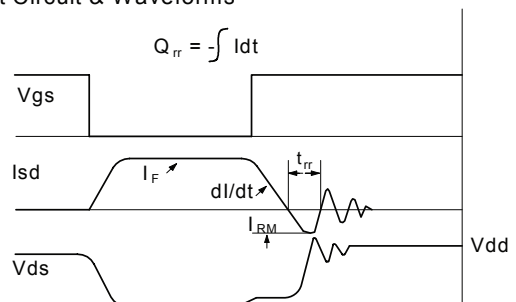
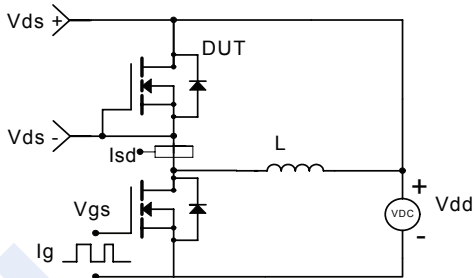
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



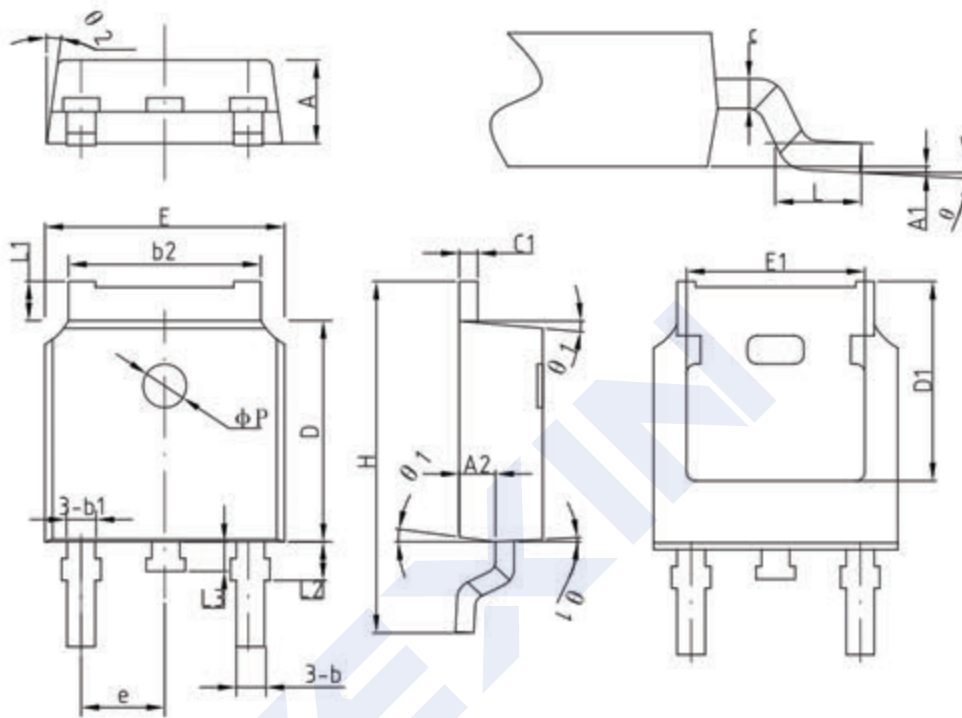
N-Channel MOSFET

2KK5092

■ Package Dimension

TO-252

Units: mm



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	2.2	2.30	2.38
A1	0	-	0.10
A2	0.90	1.01	1.10
b	0.71	0.76	0.86
b1		0.76	
b2	5.13	5.33	5.46
c	0.47	0.50	0.60
c1	0.47	0.50	0.60
D	6.0	6.10	6.20
D1	-	5.30	-
E	6.50	6.60	6.70
E1	-	4.80	-
e	2.286BSC		
H	9.70	10.10	10.40
L	1.40	1.50	1.70
L1	0.90	-	1.25
L2		1.05	
L3		0.8	
φP		1.2	
θ	0°	-	8°
θ 1	5°	7°	9°
θ 2	5°	7°	9°