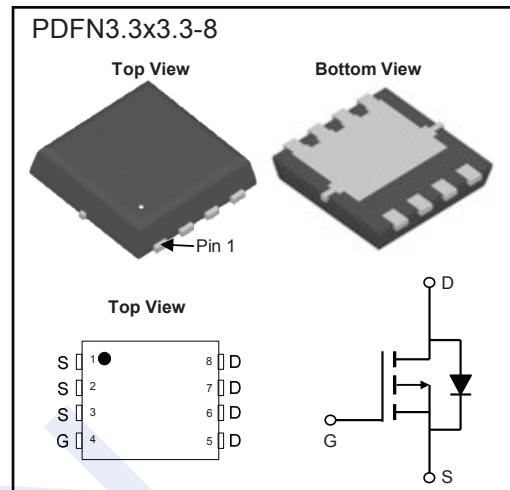


## P-Channel MOSFET

## 2KJ7115DFN

## ■ Features

- $V_{DS}$  -40 V
- $I_D$  (at  $V_{GS}=-10V$ ) -7.2 A
- $R_{DS(ON)}$  (at  $V_{GS} = - 10V$ ) < 25 mΩ
- $R_{DS(ON)}$  (at  $V_{GS} = - 4.5V$ ) < 45 mΩ

■ Absolute Maximum Ratings ( $T_A = 25^\circ C$  unless otherwise noted.)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current (Note 2)	$I_D$	-7.2	A
		-5.77	
Pulsed Drain Current (Note 3)	$I_{DM}$	-80	
Maximum Body Diode Forward Current (Note 2)	$I_S$	-7.2	
Pulsed Source Current (Note 3)	$I_{SM}$	-80	
Power Dissipation	$P_D$	0.81	W
		1.95	
Thermal Resistance, Junction- to-Ambient	$R_{\theta JA}$	155	°C/W
		64	
Junction Temperature	$T_J$	150	°C
Storage Temperature Range	$T_{Stg}$	-55 to 150	

Notes:

1. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
2. For a device surface mounted on 25mm x 25mm FR-4 PCB with 2oz copper, in still air conditions.
3. Same as note 2, except the device is pulsed with  $D= 0.02$  and pulse width 300μs.

## P-Channel MOSFET

## 2KJ7115DFN

■ Electrical Characteristics ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{I}_D = -250\mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	-40			V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}} = -40\text{V}, \text{V}_{\text{GS}} = 0\text{V}$		-1		$\mu\text{A}$
Gate-Body Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = \pm 20\text{V}$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{th})}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = -250\mu\text{A}$	-0.8		-1.8	V
Static Drain-Source On-Resistance (Note 4)	$\text{R}_{\text{DS}(\text{on})}$	$\text{V}_{\text{GS}} = -10\text{V}, \text{I}_D = -3\text{A}$		18	25	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = -4.5\text{V}, \text{I}_D = -3\text{A}$		30	45	
Forward Transconductance (Note 4,5)	$\text{g}_{\text{FS}}$	$\text{V}_{\text{DS}} = -5\text{V}, \text{I}_D = -3\text{A}$		16.6		S
Diode Forward Voltage (Note 4)	$\text{V}_{\text{SD}}$	$\text{I}_S = -1\text{ A}, \text{V}_{\text{GS}} = 0\text{V}$		-0.7	-1.0	V
<b>DYNAMIC CHARACTERISTICS (Note 5)</b>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}} = -20\text{V}, \text{f} = 1\text{MHz}$		1643		pF
Output Capacitance	$\text{C}_{\text{oss}}$			179		
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$			128		
Gate resistance	$\text{R}_g$	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=0\text{V}, \text{f}=1\text{MHz}$		6.43		$\Omega$
Total Gate Charge ( $\text{V}_{\text{GS}} = -10\text{V}$ )	$\text{Q}_g$	$\text{V}_{\text{DS}} = -20\text{V}, \text{I}_D = -3\text{A}$		14.0		nC
Total Gate Charge ( $\text{V}_{\text{GS}} = -4.5\text{V}$ )				33.7		
Gate Source Charge	$\text{Q}_{\text{gs}}$			5.5		
Gate Drain Charge	$\text{Q}_{\text{gd}}$			7.3		
Turn-On Delay Time	$\text{t}_{\text{d}(\text{on})}$			6.9		ns
Turn-On Rise Time	$\text{t}_r$	$\text{V}_{\text{GS}} = -10\text{V}, \text{V}_{\text{DS}} = -20\text{V}, \text{I}_D = -3\text{A}$		14.7		
Turn-Off Delay Time	$\text{t}_{\text{d}(\text{off})}$			53.7		
Turn-Off Fall Time	$\text{t}_f$			30.9		

Notes:

4. Measured under pulsed conditions. Pulse width  $\leq 300\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
5. For design aid only, not subject to production testing.
6. Switching characteristics are independent of operating junction temperatures.

## ■ Marking

Marking	J7115 KC****
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## P-Channel MOSFET

### 2KJ7115DFN

#### ■ Typical Characteristics

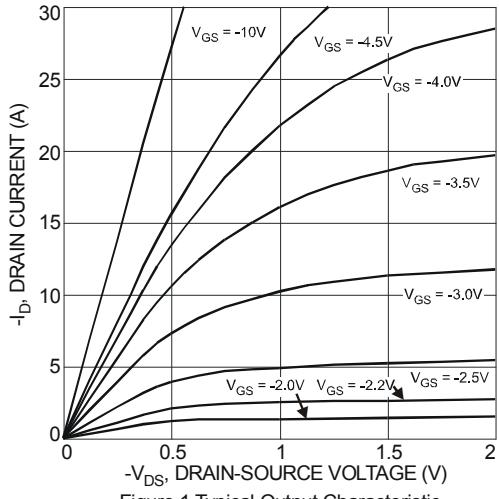


Figure 1 Typical Output Characteristic

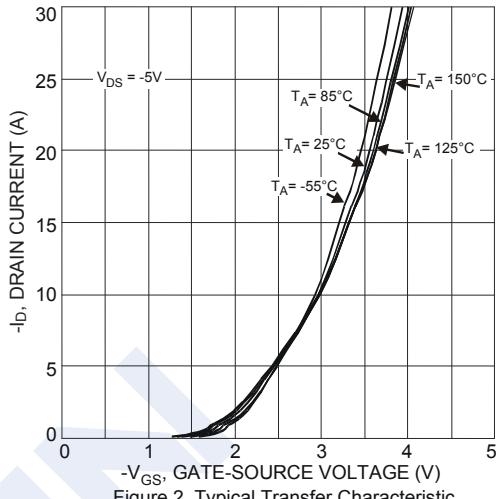


Figure 2 Typical Transfer Characteristic

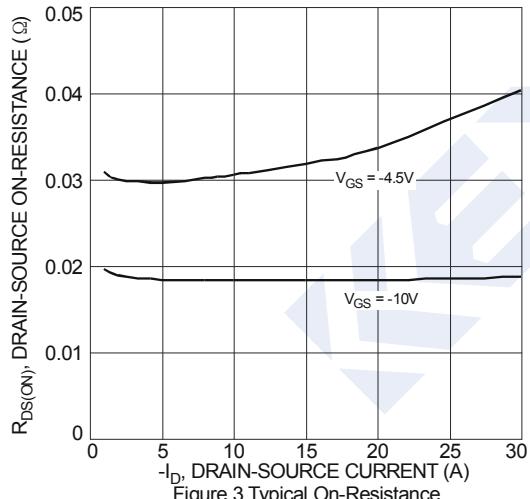


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

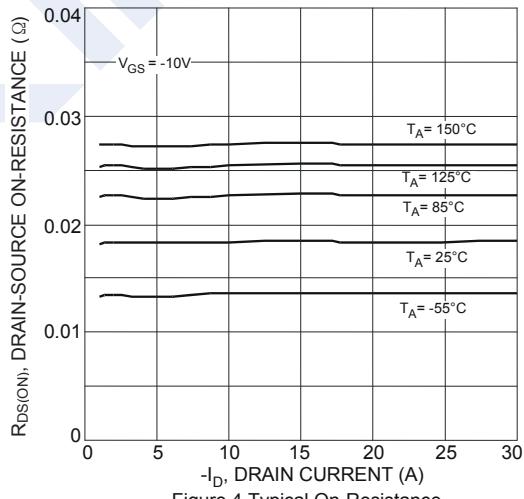


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

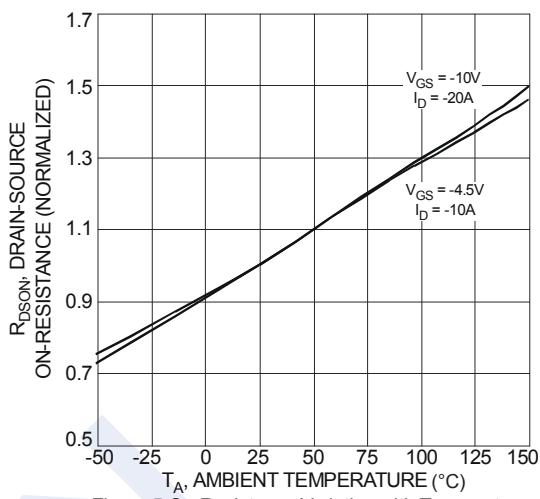


Figure 5 On-Resistance Variation with Temperature

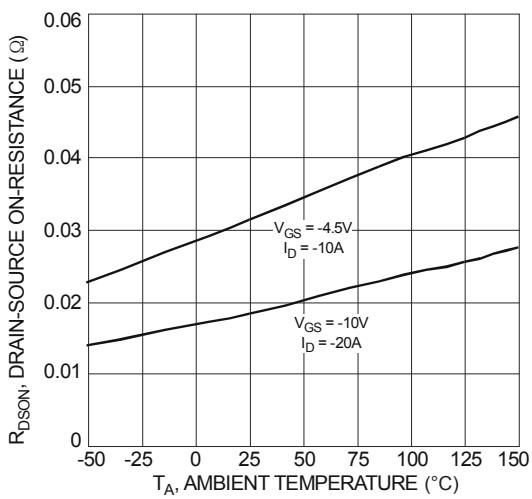


Figure 6 On-Resistance Variation with Temperature

## P-Channel MOSFET

### 2KJ7115DFN

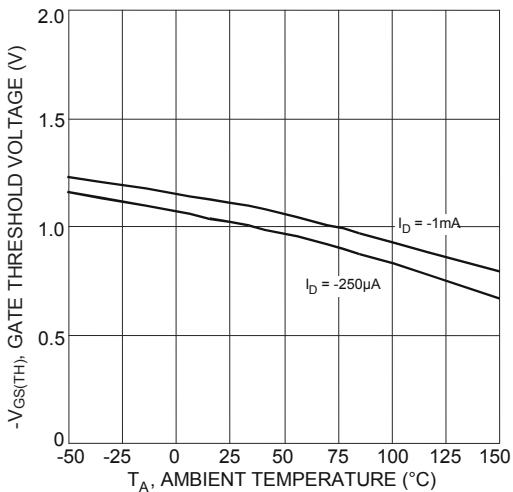


Figure 7 Gate Threshold Variation vs. Ambient Temperature

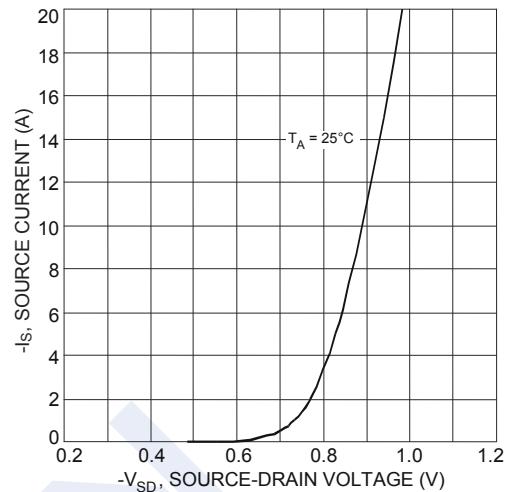


Figure 8 Diode Forward Voltage vs. Current

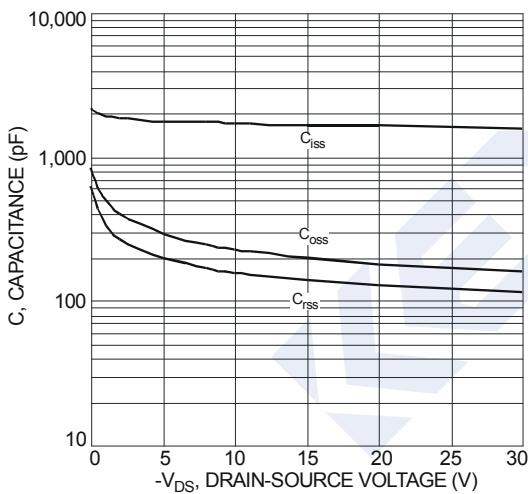


Figure 9 Typical Total Capacitance

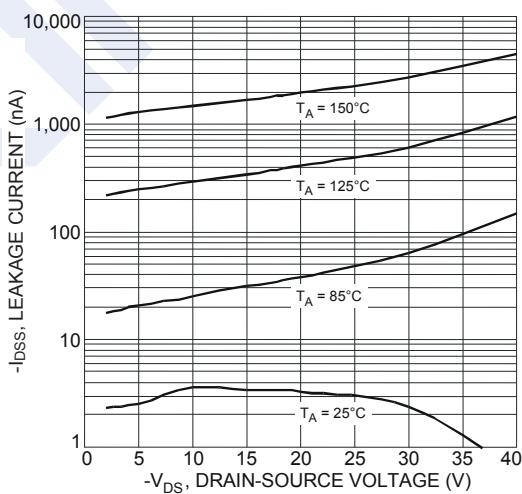


Figure 10 Typical Leakage Current vs. Drain-Source Voltage

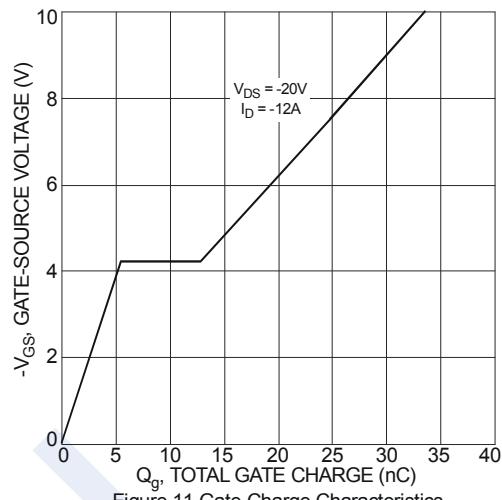


Figure 11 Gate-Charge Characteristics

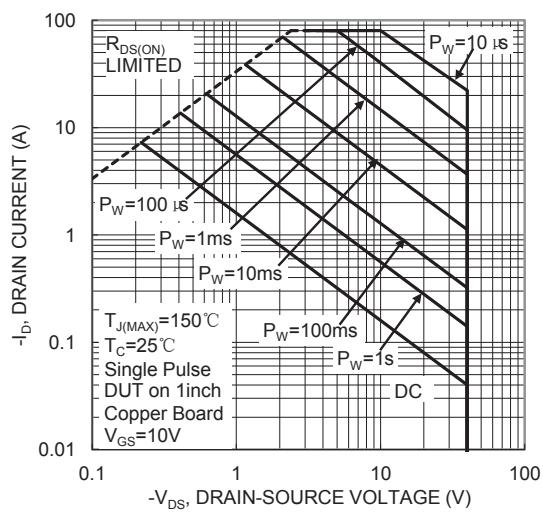
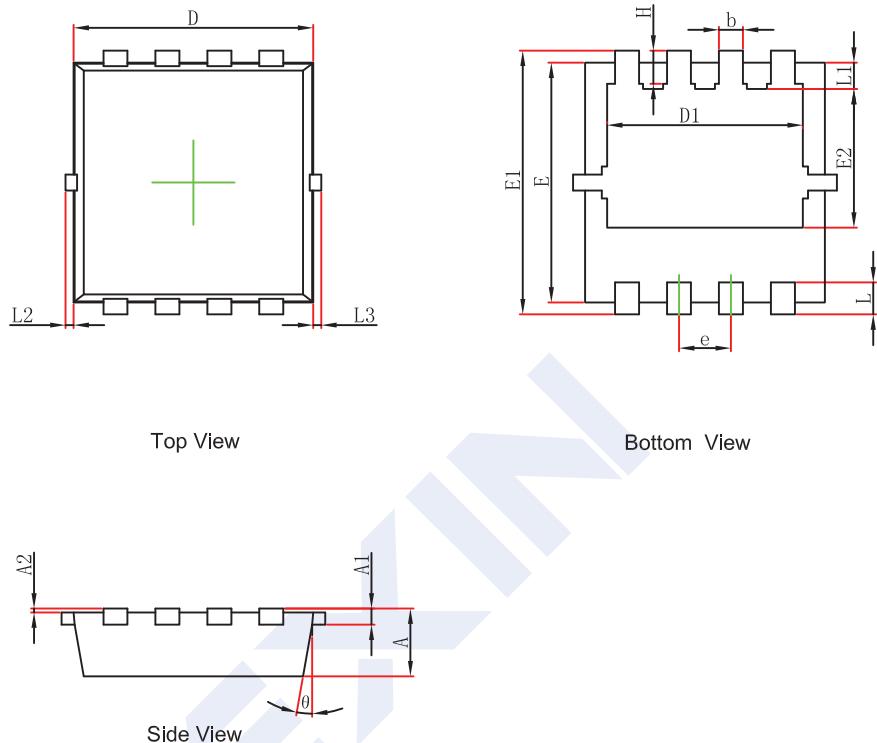


Figure 12. SOA, Safe Operation Area

## P-Channel MOSFET

## 2KJ7115DFN

## ■ PDFN3.3x3.3-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
$\theta$	9°	13°	9°	13°