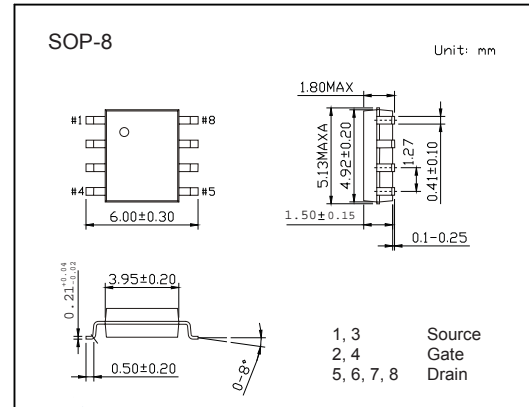
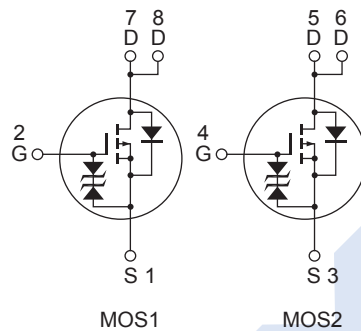


Dual P-Channel MOSFET

2KJ6030

■ Features

- $V_{DS} (V) = -20V$
- $I_D = -6A$
- $R_{DS(ON)} < 30m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)} < 50m\Omega @ V_{GS} = -2.5V$



■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current	I_D	-6	A
Pulsed Drain Current (Note 1)	I_{DP}	-48	
Power Dissipation (Note 2)	P_D	2	W
Power Dissipation (Note 3)		3	
Junction Temperature	T_J	150	$^{\circ}C$
Junction Storage Temperature Range	T_{stg}	-55 to 150	

Note 1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$

2. 1 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), $PW \leq 10 s$

3. 2 Drive operation: When using the glass epoxy board (FR4 40 × 40 × 1.6 mm), $PW \leq 10 s$

Dual P-Channel MOSFET

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■ Electrical Characteristics ($T_a = 25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = -10\text{mA}$, $V_{GS} = 0\text{V}$	-20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -20\text{V}$, $V_{GS} = 0\text{V}$			-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 10\text{V}$			± 10	
Gate Threshold Voltage	$V_{GS(off)}$	$V_{DS} = -10\text{V}$, $I_D = -1\text{mA}$	-0.4		-1.4	V
Static Drain-Source On-Resistance (Note 4)	$R_{DS(on)}$	$V_{GS} = -4.5\text{V}$, $I_D = -3\text{A}$			30	$\text{m}\Omega$
		$V_{GS} = -2.5\text{V}$, $I_D = -3\text{A}$			50	
Forward Transconductance (Note 4)	g_{FS}	$V_{DS} = -10\text{V}$, $I_D = -3\text{A}$	6			S
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}$, $V_{DS} = -10\text{V}$, $f = 1\text{MHz}$		1550		pF
Output Capacitance	C_{oss}			400		
Reverse Transfer Capacitance	C_{rss}			300		
Total Gate Charge	Q_g	$V_{DS} = -10\text{V}$, $I_D = -6\text{A}$, $V_{GS} = -4.5\text{V}$		18		nC
Gate Source Charge	Q_{gs}			3		
Gate Drain Charge	Q_{gd}			6.5		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = -10\text{V}$, $I_D = -3\text{A}$, $V_{GS} = -4.5\text{V}$, $R_L = 3.3\ \Omega$, $R_G = 4.7\ \Omega$		25		ns
Turn-On Rise Time	t_r			50		
Turn-Off Delay Time	$t_{d(off)}$			85		
Turn-Off Fall Time	t_f			40		
Diode Forward Voltage (Note 4)	V_{SD}	$I_{SD} = -6\text{A}$, $V_{GS} = 0\text{V}$			-1.1	V
Reverse Recovery Time	t_{rr}	$I_F = -6\text{A}$, $V_{GS} = 0$ $di/dt = 20\text{A}/\mu\text{s}$		60		nS

Note 4. Pulse Test

■ Marking

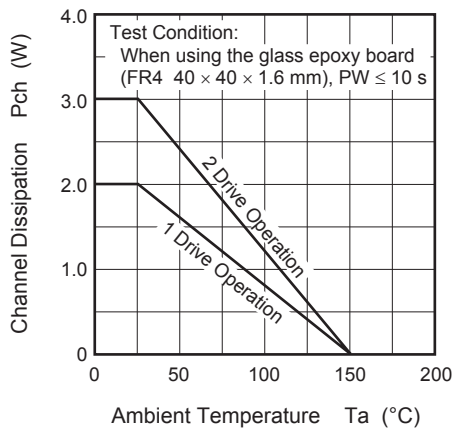
Marking	J6030 KA***
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Dual P-Channel MOSFET

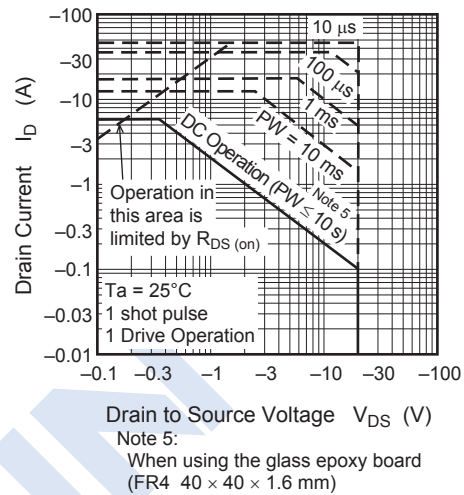
2KJ6030

■ Typical Characteristics

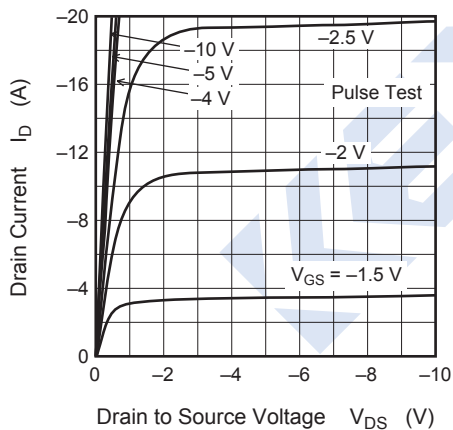
Power vs. Temperature Derating



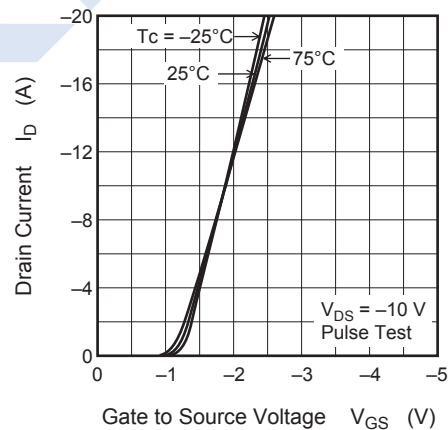
Maximum Safe Operation Area



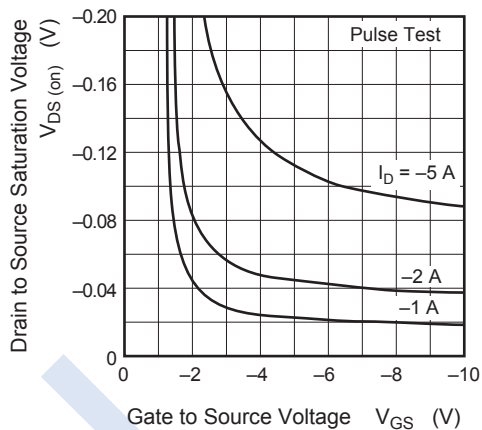
Typical Output Characteristics



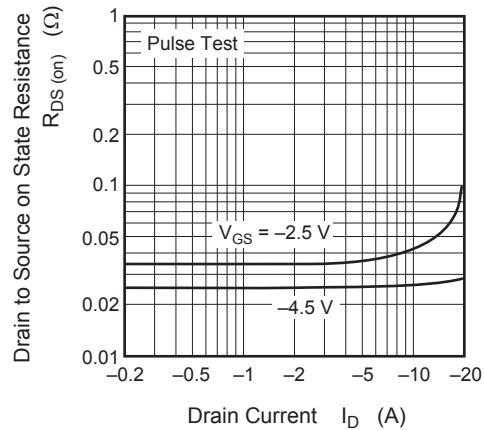
Typical Transfer Characteristics



Drain to Source Saturation Voltage vs. Gate to Source Voltage

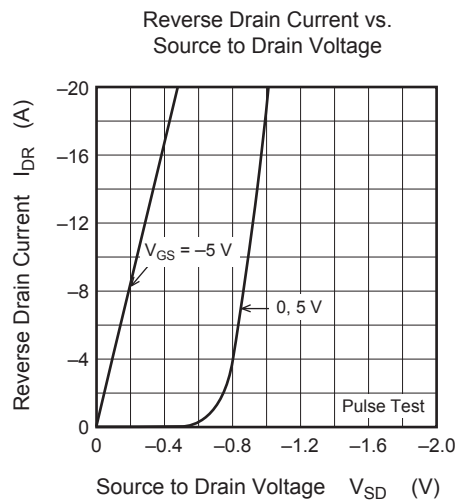
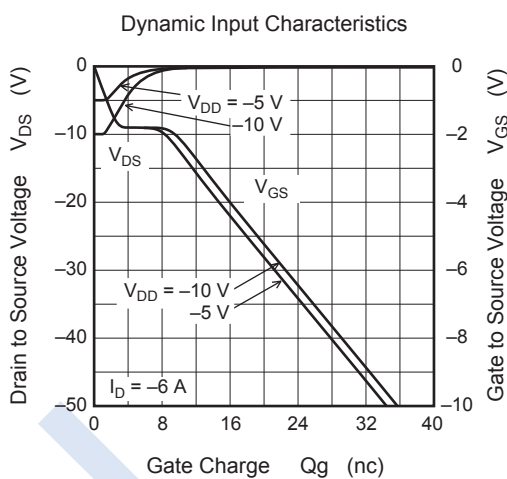
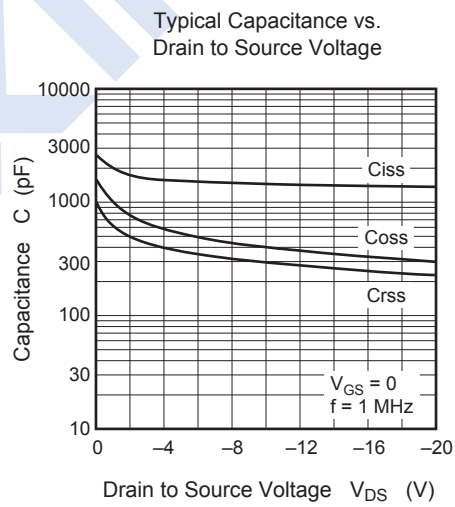
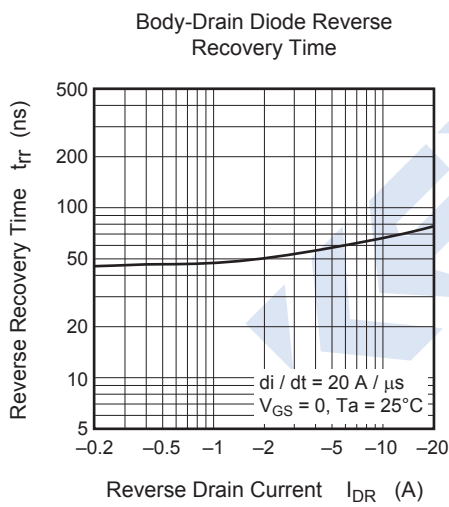
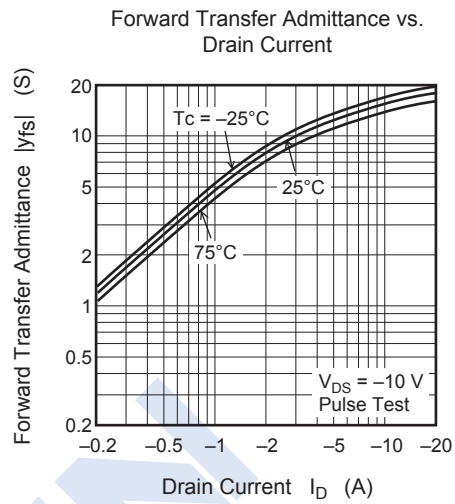
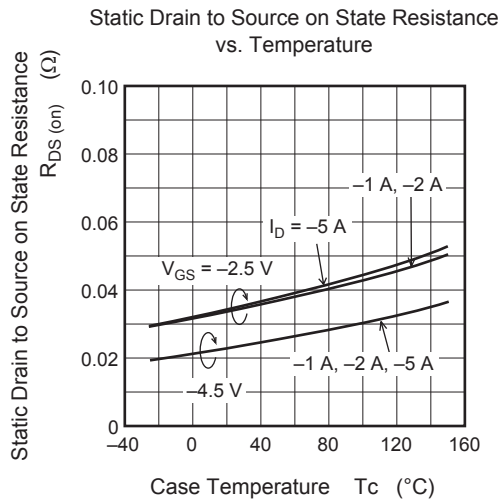


Static Drain to Source on State Resistance vs. Drain Current



Dual P-Channel MOSFET

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Dual P-Channel MOSFET

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