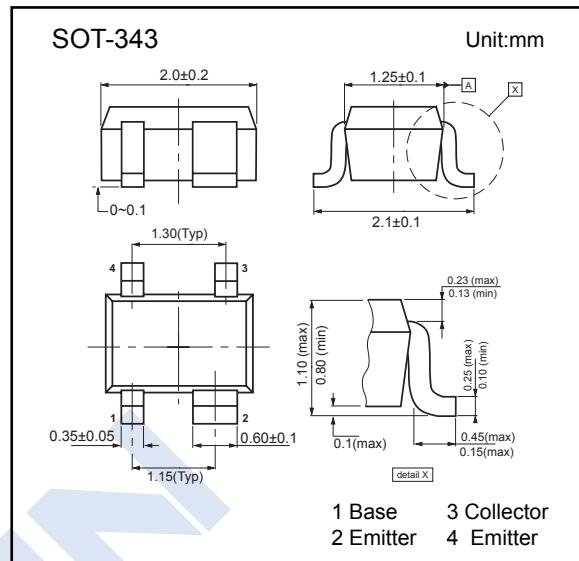


NPN Silicon RF Transistor

2KC1106

■ Features

- For high gain low noise amplifiers
- For oscillators up to 10 GHz
- Noise figure F = 1.05 dB at 1.8 GHz outstanding G_{ms} = 20 dB at 1.8 GHz
- Transition frequency f_r = 25 GHz
- Gold metalization for high reliability

■ Absolute Maximum Ratings (T_A = 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Collector - Base Voltage	V _{CBO}	15	V
Collector - Emitter Voltage	V _{CEO}	4.5	
Emitter - Base Voltage	V _{EBO}	1.5	
Collector Current	I _C	35	mA
Base current	I _B	3	
Total Power Dissipation) Ts ≤ 107°C	P _{tot}	160	mW
Junction - soldering point *1	R _{θJS}	270	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature range	T _{stg}	-65 to 150	

*1 Ts is measured on the collector lead at the soldering point to the pcb.

NPN Silicon RF Transistor**2KC1106****■ Electrical Characteristics ($T_A = 25^\circ\text{C}$, unless otherwise specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Collector- base breakdown voltage	V_{CBO}	$I_C = 100 \mu\text{A}, I_E = 0$	15			V
Collector- emitter breakdown voltage	V_{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	4.5			
Emitter - base breakdown voltage	V_{EBO}	$I_E = 100 \mu\text{A}, I_C = 0$	1.5			
Collector-base cut-off current	I_{CBO}	$V_{CB} = 5 \text{ V}, I_E = 0$			200	nA
Emitter cut-off current	I_{EBO}	$V_{EB} = 1.5 \text{ V}, I_C = 0$			35	μA
DC current gain	h_{FE}	$V_{CE} = 4 \text{ V}, I_C = 20 \text{ mA}$	50		150	
Collector-base capacitance	C_{Cb}	$V_{CB} = 2 \text{ V}, f = 1 \text{ MHz}$		0.15	0.24	pF
Collector emitter capacitance	C_{ce}	$V_{CE} = 2 \text{ V}, f = 1 \text{ MHz}$		0.41		
Emitter-base capacitance	C_{eb}	$V_{EB} = 0.5 \text{ V}, f = 1 \text{ MHz}$		0.55		
Noise figure	F	$I_C = 5 \text{ mA}, V_{CE} = 2 \text{ V}, Z_s = Z_{\text{sopt}}, f = 1.8 \text{ GHz}$		1.05	1.4	dB
Power gain ^{*1}	G_{ms}	$I_C = 20 \text{ mA}, V_{CE} = 2 \text{ V}, Z_s = Z_{\text{sopt}}, Z_L = Z_{\text{lopt}}, f = 1.8 \text{ GHz}$		20		
Insertion power gain	$ S_{21} _2$	$I_C = 20 \text{ mA}, V_{CE} = 2 \text{ V}, Z_s = Z_L = 50\Omega, f = 1.8 \text{ GHz}$	14	17		
Third order intercept point	IP_3	$I_C = 20 \text{ mA}, V_{CE} = 2 \text{ V}, Z_s = Z_{\text{sopt}}, Z_L = Z_{\text{lopt}}, f = 1.8 \text{ GHz}$		22		dBm
1dB Compression point at output	$P_{-1\text{dB}}$	$I_C = 20 \text{ mA}, V_{CE} = 2 \text{ V}, Z_s = Z_{\text{sopt}}, Z_L = Z_{\text{lopt}}$		12		
Transition frequency	f_T	$I_C = 30 \text{ mA}, V_{CE} = 3 \text{ V}, f = 2 \text{ GHz}$	20	25		GHz

*1 $G_{ms} = |S_{21}| / S_{12}|$ **■ Marking**

Marking	AMs
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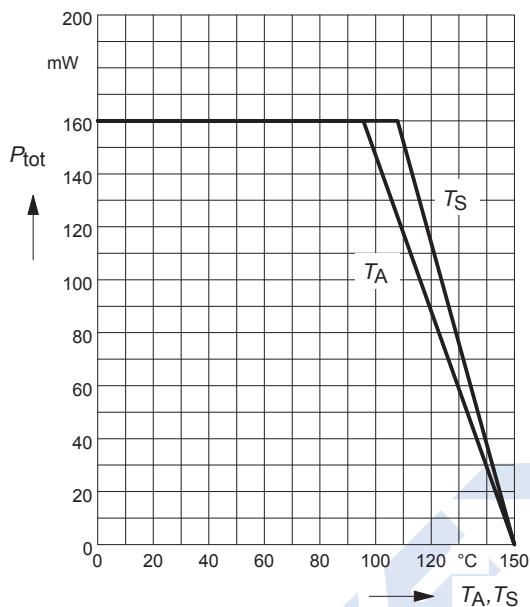
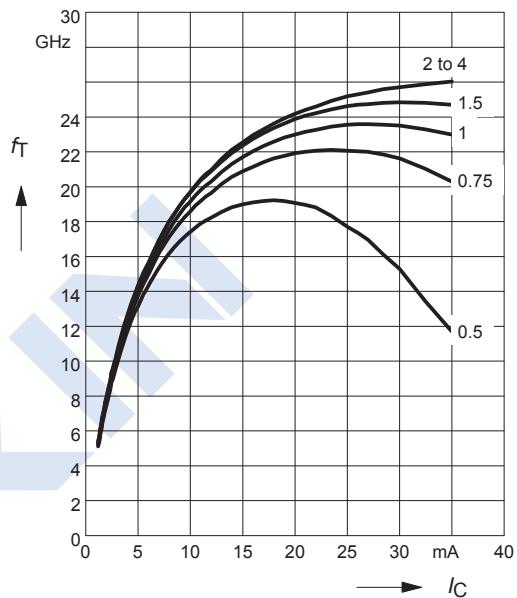
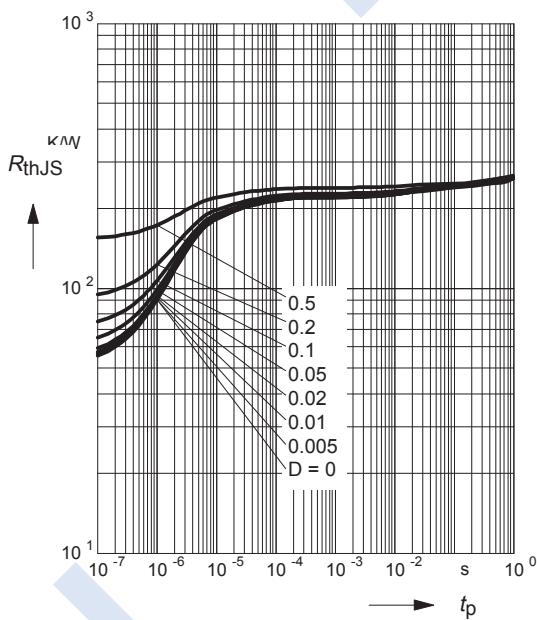
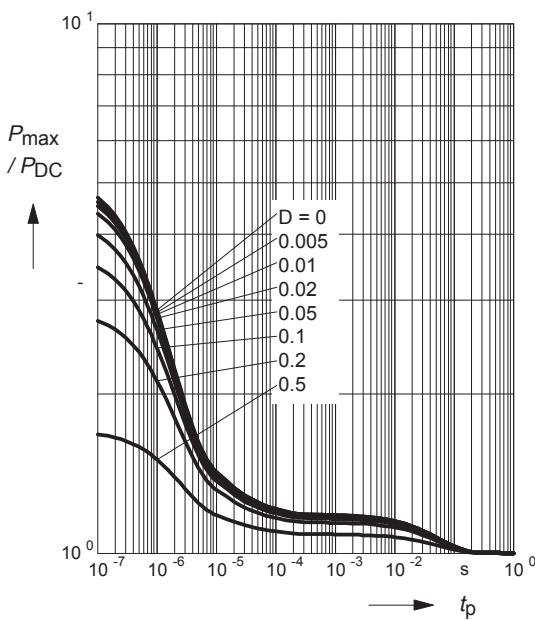
NPN Silicon RF Transistor

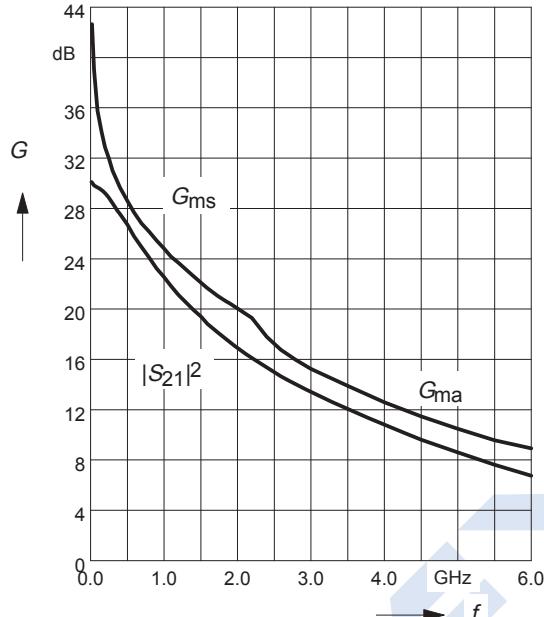
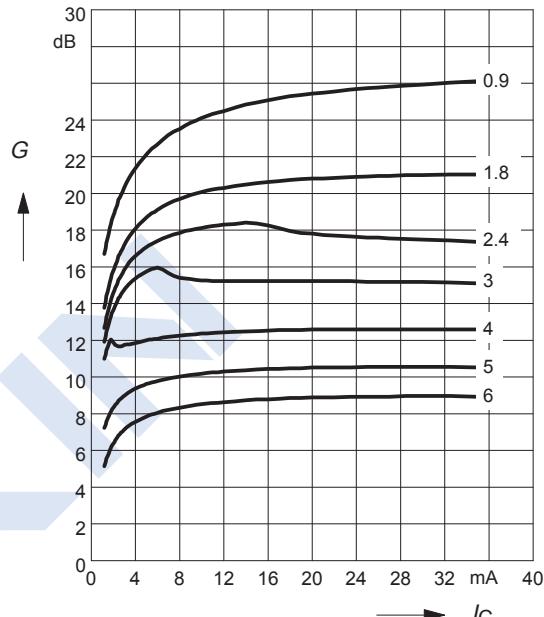
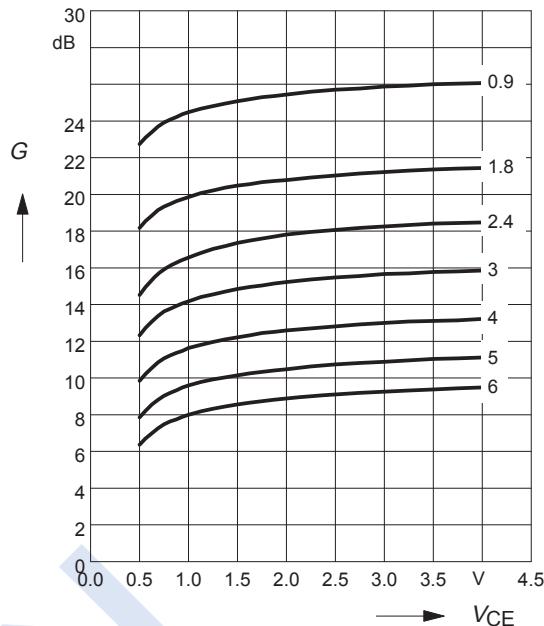
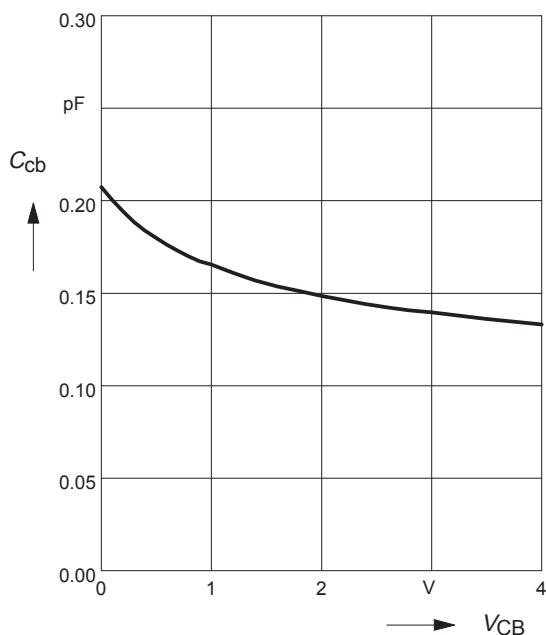
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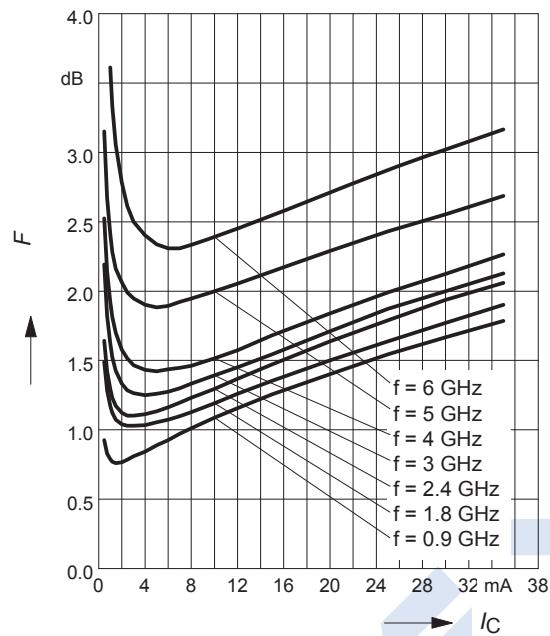
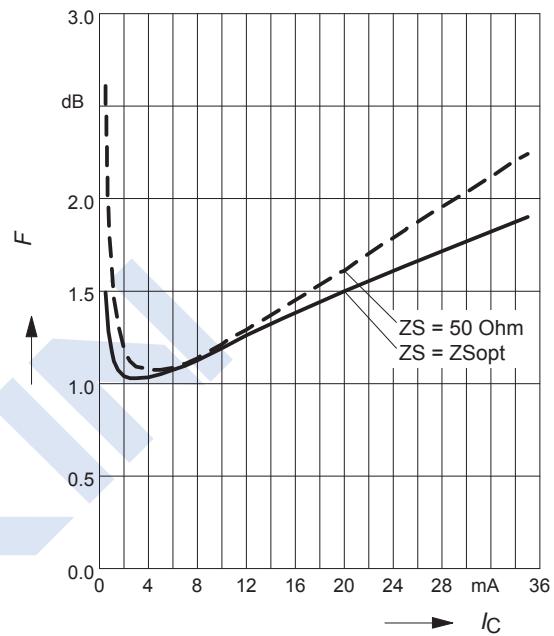
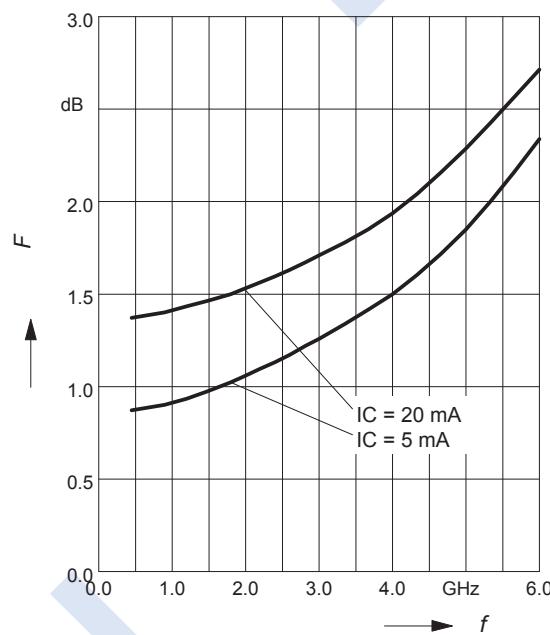
■ Electrical characteristic curves

Total power dissipation $P_{\text{tot}} = f(T_A^*, T_S)$

* Package mounted on epoxy

**Transition frequency** $f_T = f(I_C)$ $f = 2 \text{ GHz}$ $V_{\text{CE}} = \text{parameter in V}$ **Permissible Pulse Load** $R_{\text{thJS}} = f(t_p)$ **Permissible Pulse Load** $P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$ 

NPN Silicon RF Transistor**2KC1106****Power gain $G_{ma}, G_{ms}, |S_{21}|^2 = f(f)$** $V_{CE} = 2V, I_C = 20 \text{ mA}$ **Power gain $G_{ma}, G_{ms} = f(I_C)$** $V_{CE} = 2V$ $f = \text{parameter in GHz}$ **Power gain $G_{ma}, G_{ms} = f(V_{CE})$** $I_C = 20 \text{ mA}$ $f = \text{parameter in GHz}$ **Collector-base capacitance $C_{cb} = f(V_{CB})$** $V_{BE} = 0, f = 1\text{MHz}$ 

NPN Silicon RF Transistor**2KC1106****Noise figure $F = f(I_C)$** $V_{CE} = 2 \text{ V}, Z_S = Z_{\text{Sopt}}$ **Noise figure $F = f(I_C)$** $V_{CE} = 2 \text{ V}, f = 1.8 \text{ GHz}$ **Noise figure $F = f(f)$** $V_{CE} = 2 \text{ V}, Z_S = Z_{\text{Sopt}}$ **Source impedance for min.****Noise Figure versus Frequency** $V_{CE} = 2 \text{ V}, I_C = 5 \text{ mA} / 20 \text{ mA}$ 